

# DPDK Intel Cryptodev and IPSec Performance Report Release 20.05

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**Author:** Intel DPDK Validation team



## Revision History

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Date	Revision	Comment
June 22, 2020	1.0	Initial document for release



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## Audience and Purpose

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The primary audience for this test report are architects and engineers implementing the Data Plane Development Kit (DPDK). This report provides information on packet processing performance testing for the specified DPDK release on Intel® architecture. The initial report may be viewed as the baseline for future releases and provides system configuration and test cases based on DPDK examples.

The purpose of reporting these tests is not to imply a single “correct” approach, but rather to provide a baseline of well-tested configurations and procedures with reproducible results. This will help guide architects and engineers who are evaluating and implementing DPDK solutions on Intel® architecture and can assist in achieving optimal system performance.

## Cryptodev Test setup:

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The device under test (DUT) consists of a system with an Intel® architecture motherboard populated with the following;

- A single or dual processor and PCH chip, except for System on Chip (SoC) cases
- DRAM memory size and frequency (normally single DIMM per channel)
- Specific Intel Network Interface Cards (NICs)
- BIOS settings noting those that updated from the basic settings
- DPDK build configuration settings, and commands used for tests

Benchmarking a DPDK system requires knowledge of networking technologies including knowledge of network protocols and hands-on experience with relevant open-source software, such as Linux\*, and the DPDK. Engineers also need benchmarking and debugging skills, as well as a good understanding of the device-under-test (DUT) across compute and networking domains.

**dpdk-test-crypto-perf Application:** Documentation may be found at <http://dpdk.org/doc/guides/tools/cryptoperf.html>.

The dpdk-test-crypto-perf tool is a Data Plane Development Kit (DPDK) utility that allows measuring performance parameters of PMDs available in the crypto tree. There are available for two measurement types: throughput and latency. Users can use multiple cores to run tests on but only one type of crypto PMD can be measured during single application execution. Cipher parameters, type of device, type of operation and chain mode have to be specified in the command line as application parameters. These parameters are checked using device capabilities structure.

Below is an example setup topology for the performance test. Generally, Cores, memories, Intel QuickAssist Technology hardware are connected to same socket. The performance result for multi-core testing sums each core’s throughput number.

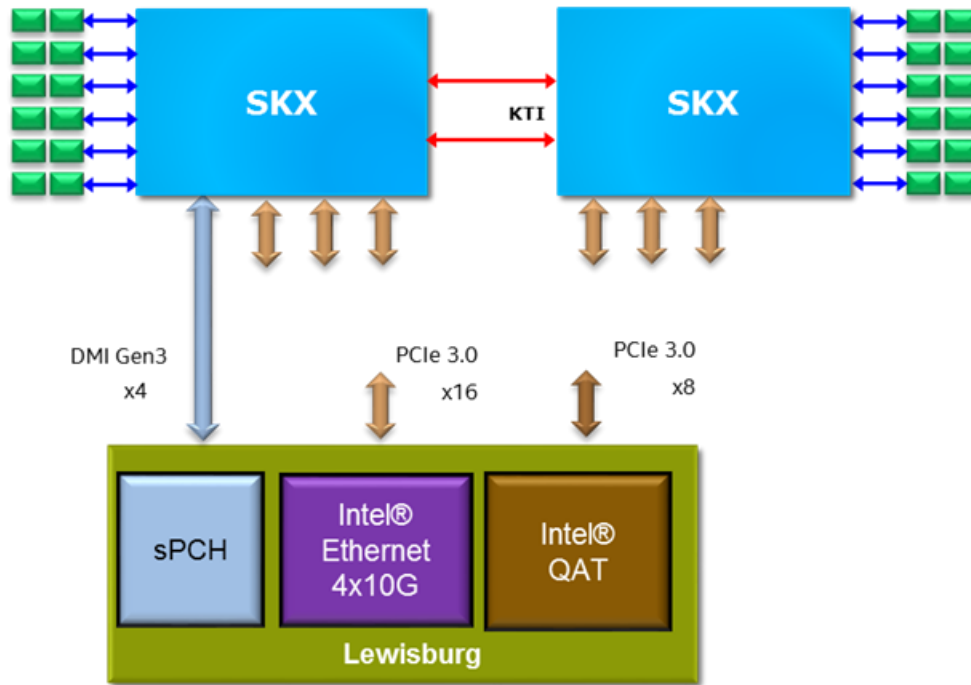
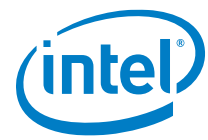
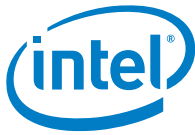


Figure1. DPDK cryptodev performance test setup



# Intel® Xeon® Platinum 8180 Processor (38.5M Cache, 2.50 GHz)

## Hardware & Software Ingredients

Item	Description
Server Platform	PURLEY
Chipset	<a href="#">Intel® C620 Series Chipset</a>
CPU	Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz <a href="https://ark.intel.com/products/120496/Intel-Xeon-Platinum-8180-Processor-38_5M-Cache-2_50-GHz">https://ark.intel.com/products/120496/Intel-Xeon-Platinum-8180-Processor-38_5M-Cache-2_50-GHz</a> Number of cores 28, Number of threads 56.
Memory	Total 98304 MBs over 12 channels @ 2133 MHz
PCIe	3 x PCIe Gen3 x8 slots
QAT	PCI-e x16 mode
Operating System	Ubuntu18.04
BIOS	SE5C620.86B.00.01.0009.101920170742
Microcode version	0x2006906
Linux kernel version	4.15.0-101-generic
GCC version	7.5.0
DPDK version	20.05

### Boot and BIOS settings

Item	Description
Boot settings	<code>intel_iommu=on iommu=pt intel_pstate=disable isolcpus=4-7,12-15 nohz_full=4-7,12-15 rcu_nocbs=4-7,12-15</code>
BIOS	CPU Power and Performance Policy <Performance> Package C-state Disabled Hardware P-state Disabled Enhanced Intel® Speedstep® Tech Disabled Intel® Turbo Boost Technology Disabled
DPDK Settings	Build Options: config/common_base CONFIG RTE LIBRTE PMD QAT SYM=y CONFIG RTE LIBRTE PMD AESNI MB=y CONFIG RTE LIBRTE PMD AESNI GCM=y



# Test Case 1 – Cryptodev QAT(Intel QuickAssist Technology) PMD performance test

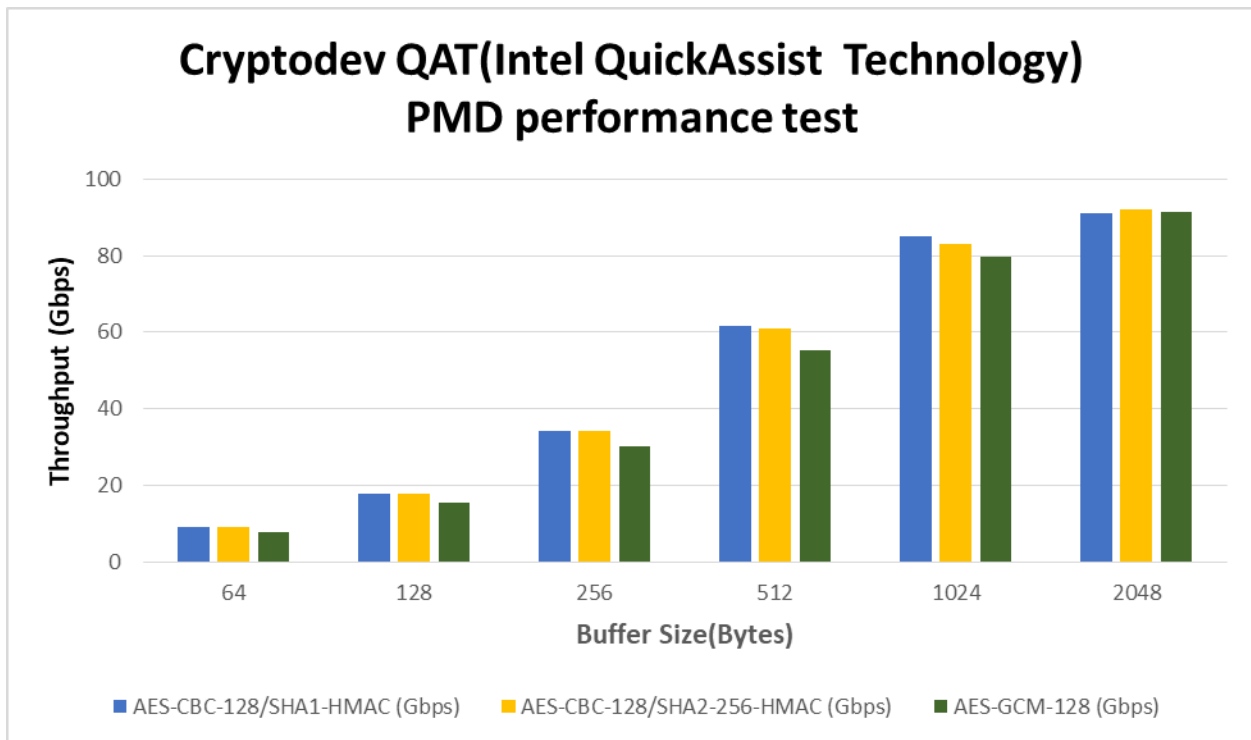
Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC with Intel QuickAssist Technology
Cores	3C6T
QAT	Integrated Intel QuickAssist Technology , PCI-e x16 Mode
Command line (AES-CBC-128/SHA1-HMAC)	<pre>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -w 0000:1a:01.0 -w 0000:1c:01.0 -w 0000:1e:01.0 -w 0000:1a:01.1 -w 0000:1c:01.1 -w 0000:1e:01.1 -w 0000:1a:01.2 -w 0000:1c:01.2 -w 0000:1e:01.2 -w 0000:1a:01.3 -w 0000:1c:01.3 -w 0000:1e:01.3 -w 0000:1a:01.4 -w 0000:1c:01.4 -w 0000:1e:01.4 -w 0000:1a:01.5 -w 0000:1c:01.5 -w 0000:1e:01.5 --vdev crypto_scheduler_pmd_1,slave=0000:1a:01.0_qat_sym,slave=0000:1c:01.0_qat_sym,slave=0000:1e:01.0_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_2,slave=0000:1a:01.1_qat_sym,slave=0000:1c:01.1_qat_sym,slave=0000:1e:01.1_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_3,slave=0000:1a:01.2_qat_sym,slave=0000:1c:01.2_qat_sym,slave=0000:1e:01.2_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_4,slave=0000:1a:01.3_qat_sym,slave=0000:1c:01.3_qat_sym,slave=0000:1e:01.3_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_5,slave=0000:1a:01.4_qat_sym,slave=0000:1c:01.4_qat_sym,slave=0000:1e:01.4_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_6,slave=0000:1a:01.5_qat_sym,slave=0000:1c:01.5_qat_sym,slave=0000:1e:01.5_qat_sym,mode=round-robin -l 9,10,66,11,67,12,68 -n 6 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_scheduler --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 30000000 --silent --digest-sz 20 --auth-algo sha1-hmac --cipher-algo aes-cbc --cipher-op encrypt</pre>
Command line (AES-CBC-128/SHA2-256-HMAC)	<pre>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -w 0000:1a:01.0 -w 0000:1c:01.0 -w 0000:1e:01.0 -w 0000:1a:01.1 -w 0000:1c:01.1 -w 0000:1e:01.1 -w 0000:1a:01.2 -w 0000:1c:01.2 -w 0000:1e:01.2 -w 0000:1a:01.3 -w 0000:1c:01.3 -w 0000:1e:01.3 -w 0000:1a:01.4 -w 0000:1c:01.4 -w 0000:1e:01.4 -w 0000:1a:01.5 -w 0000:1c:01.5 -w 0000:1e:01.5 --vdev crypto_scheduler_pmd_1,slave=0000:1a:01.0_qat_sym,slave=0000:1c:01.0_qat_sym,slave=0000:1e:01.0_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_2,slave=0000:1a:01.1_qat_sym,slave=0000:1c:01.1_qat_sym,slave=0000:1e:01.1_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_3,slave=0000:1a:01.2_qat_sym,slave=0000:1c:01.2_qat_sym,slave=0000:1e:01.2_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_4,slave=0000:1a:01.3_qat_sym,slave=0000:1c:01.3_qat_sym,slave=0000:1e:01.3_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_5,slave=0000:1a:01.4_qat_sym,slave=0000:1c:01.4_qat_sym,slave=0000:1e:01.4_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_6,slave=0000:1a:01.5_qat_sym,slave=0000:1c:01.5_qat_sym,slave=0000:1e:01.5_qat_sym,mode=round-robin -l 9,10,66,11,67,12,68 -n 6 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_scheduler --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 30000000 --silent --digest-sz 32 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</pre>
Command line (AES-GCM-128)	<pre>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -w 0000:1a:01.0 -w 0000:1c:01.0 -w 0000:1e:01.0 -w 0000:1a:01.1 -w 0000:1c:01.1 -w 0000:1e:01.1 -w 0000:1a:01.2 -w 0000:1c:01.2 -w 0000:1e:01.2 -w 0000:1a:01.3 -w 0000:1c:01.3 -w 0000:1e:01.3 -w 0000:1a:01.4 -w 0000:1c:01.4 -w 0000:1e:01.4 -w 0000:1a:01.5 -w 0000:1c:01.5 -w 0000:1e:01.5 --vdev crypto_scheduler_pmd_1,slave=0000:1a:01.0_qat_sym,slave=0000:1c:01.0_qat_sym,slave=0000:1e:01.0_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_2,slave=0000:1a:01.1_qat_sym,slave=0000:1c:01.1_qat_sym,slave=0000:1e:01.1_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_3,slave=0000:1a:01.2_qat_sym,slave=0000:1c:01.2_qat_sym,slave=0000:1e:01.2_qat_sym,mode=round-robin --vdev=crypto_scheduler_pmd_4,slave=0000:1a:01.3_qat_sym,slave=0000:1c:01.3_qat_sym,slave=0000:1e:01.3_qat_sym,mode=round-robin</pre>



	<pre>qat_sym,slave=0000:1e:01.3_qat_sym,mode=round-robin -- vdev=crypto_scheduler_pmd_5,slave=0000:1a:01.4_qat_sym,slave=0000:1c:01.4_ qat_sym,slave=0000:1e:01.4_qat_sym,mode=round-robin -- vdev=crypto_scheduler_pmd_6,slave=0000:1a:01.5_qat_sym,slave=0000:1c:01.5_ qat_sym,slave=0000:1e:01.5_qat_sym,mode=round-robin -l 9,10,66,11,67,12,68 -n 6 -- --aead-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --optype aead --ptest throughput --aead-aad-sz 16 --devtype crypto_scheduler -- aead-op encrypt --burst-sz 32 --total-ops 30000000 --silent --digest-sz 16 --aead-algo aes-gcm --aead-iv-sz 12</pre>
Notes	Use multi-cores configuration for testing is aim to reach maximum of QAT capability

Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	9.16	9.08	7.94
128	17.95	17.81	15.68
256	34.31	34.13	30.40
512	61.57	61.04	55.41
1024	84.92	82.97	79.64
2048	90.90	91.99	91.52





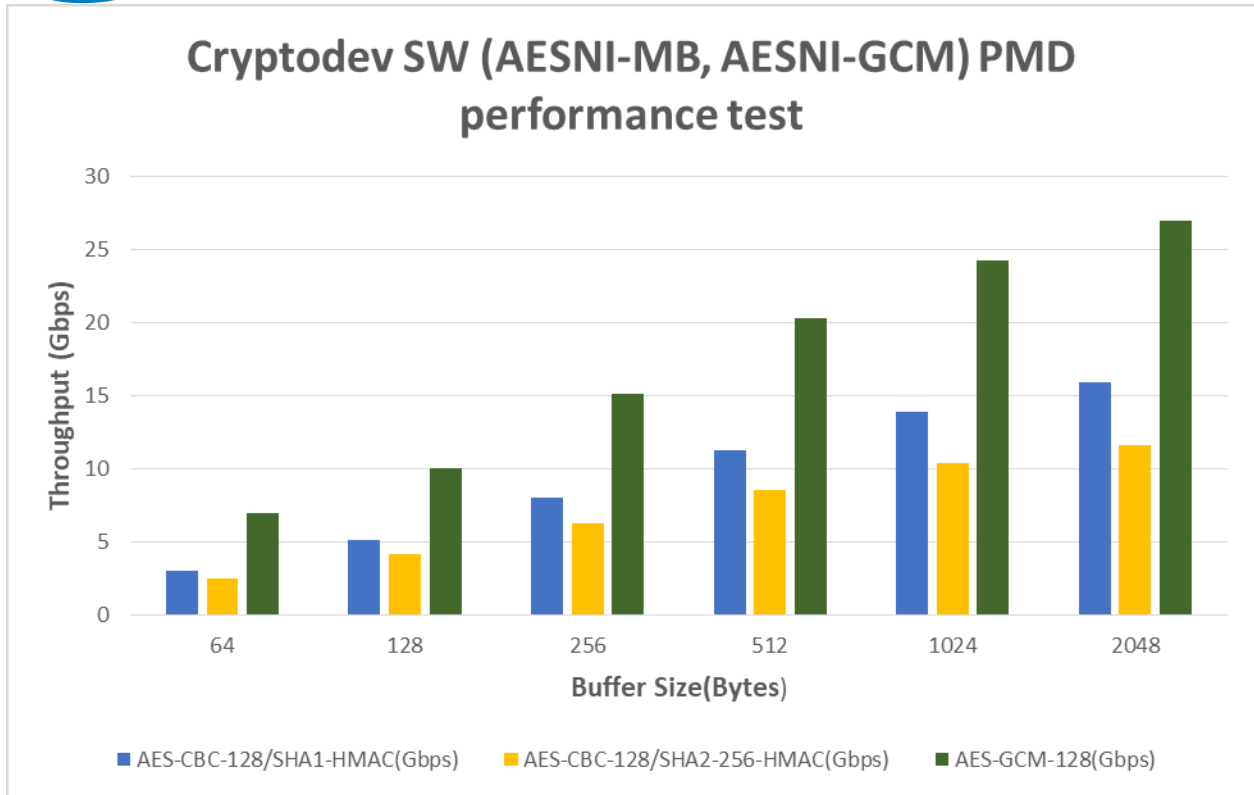


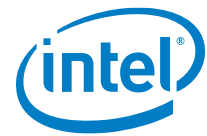
## Test Case 2 – Cryptodev SW (AESNI-MB, AESNI-GCM) PMD performance test

Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC, KASUMI-F8/KASUMI-F9, SNOW3G-UEA2/SNOW3G-UIA2, ZUC-EEA3/ZUC-EIA3
Cores	1C1T
QAT	Not use
Command line (AES-CBC-128/SHA1-HMAC)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 9,10 -n 6 -w 0000:1a:01.0 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 10000000 --silent --digest-sz 12 --auth-algo sha1-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-CBC-128/SHA2-256-HMAC)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 9,10 -n 6 -w 0000:1a:01.0 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 10000000 --silent --digest-sz 16 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-GCM-128)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_gcm_pmd_1 -l 9,10 -n 6 -w 0000:1a:01.0 -- --aead-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --optype aead --ptest throughput --aead-aad-sz 16 --devtype crypto_aesni_gcm --aead-op encrypt --burst-sz 32 --total-ops 10000000 --silent --digest-sz 16 --aead-algo aes-gcm --aead-iv-sz 12</code>
Notes	The SW PMD performance is linear scaling out with core numbers. The scale factor is around 1. If the hyper-threading is enabled, extra ~20%-50% performance will be achieved per hyper-thread.

### Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	3.00	2.47	6.95
128	5.14	4.14	10.00
256	8.05	6.31	15.16
512	11.22	8.54	20.26
1024	13.89	10.36	24.24
2048	15.87	11.60	26.97





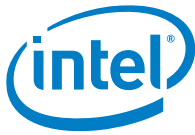
# Intel® Xeon® Processor D-1553N (12M Cache, 2.30 GHz)

## Hardware & Software Ingredients

Item	Description
Server Platform	GRANGEVILLE
CPU	Intel® Xeon® Processor D-1553N (12M Cache, 2.30 GHz) <a href="https://ark.intel.com/products/123002/Intel-Xeon-Processor-D-1553N-12M-Cache-2_30-GHz">https://ark.intel.com/products/123002/Intel-Xeon-Processor-D-1553N-12M-Cache-2_30-GHz</a> Number of cores 8, Number of threads 16.
Memory	Total 65536 MBs over 4 channels @ 2400 MHz
Operating System	Ubuntu 18.04.3
BIOS	GNVDTRL1.86B.0010.D51.1706230411
Microcode version	0xe00000d
Linux kernel version	5.0.0-23-generic
GCC version	7.5.0
DPDK version	20.05

### Boot and BIOS settings

Item	Description
Boot settings	intel_iommu=on iommu=pt intel_pstate=disable isolcpus=4-7,12-15 nohz_full=4-7,12-15 rcu_nocbs=4-7,12-15 hugepagesz=1G hugepages=10 default_hugepagesz=1G
BIOS	Boot performance mode <Max Performance> CPU C state Disabled Energy efficient P-state Disabled Turbo Mode Disabled
DPDK Settings	Build Options: config/common_base CONFIG_RTE_LIBRTE_PMD_QAT_SYM=y CONFIG_RTE_LIBRTE_PMD_AESNI_MB=y CONFIG_RTE_LIBRTE_PMD_AESNI_GCM=y

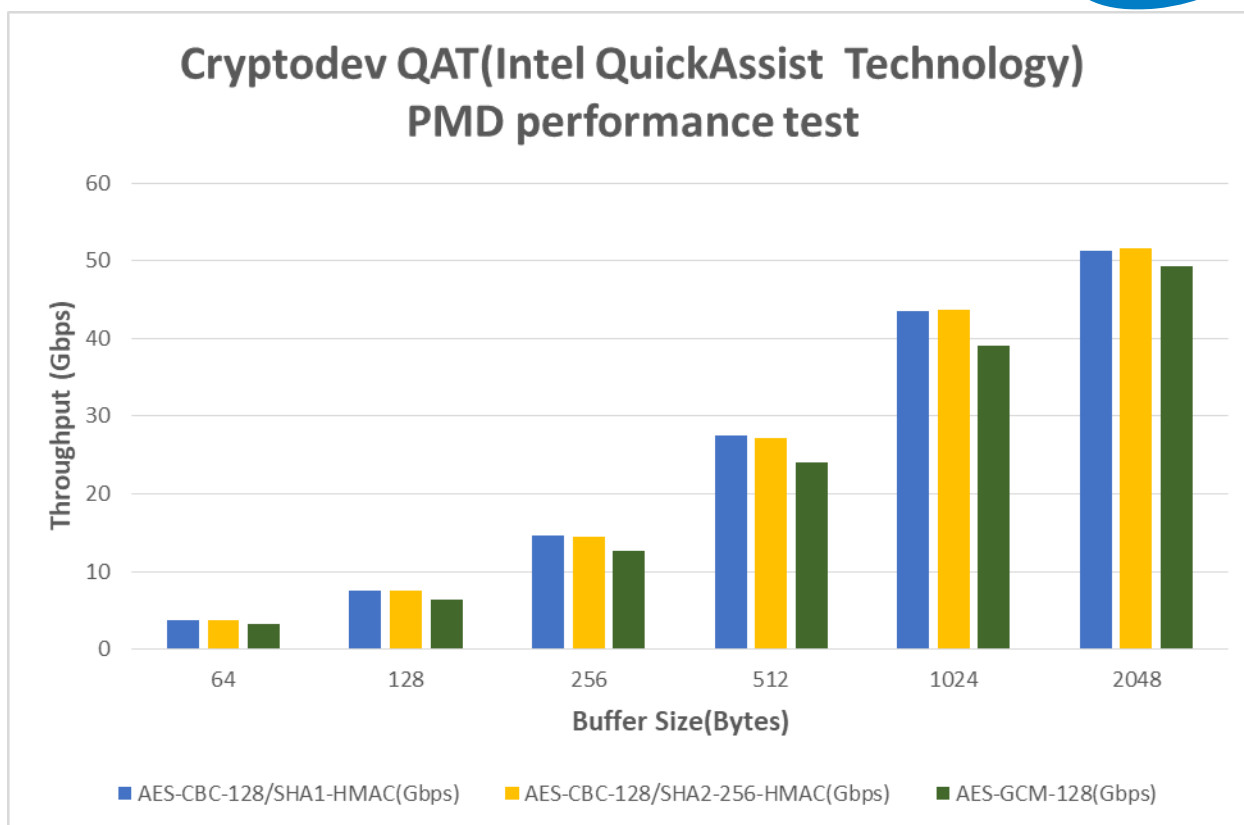


## Test Case 3 – Cryptodev QAT(Intel QuickAssist Technology) PMD performance test

Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC by Intel QuickAssist Technology
Cores	2C4T
QAT	Integrated Intel QuickAssist Technology
Command line (AES-CBC-128/SHA1-HMAC)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -w 0000:02:01.0 -w 0000:02:01.1 -w 0000:02:01.2 -w 0000:02:01.3 -l 4,5,13,6,14 -n 4 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_qat --cipher-iv-sz 16 -auth-op generate --burst-sz 32 --total-ops 30000000 --silent --digest-sz 20 --auth-algo sha1-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-CBC-128/SHA2-256-HMAC)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -w 0000:02:01.0 -w 0000:02:01.1 -w 0000:02:01.2 -w 0000:02:01.3 -l 4,5,13,6,14 -n 4 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_qat --cipher-iv-sz 16 -auth-op generate --burst-sz 32 --total-ops 30000000 --silent --digest-sz 32 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-GCM-128)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -w 0000:02:01.0 -w 0000:02:01.1 -w 0000:02:01.2 -w 0000:02:01.3 -l 4,5,13,6,14 -n 4 -- --aead-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --optype aead --ptest throughput --aead-aad-sz 16 --devtype crypto_qat --aead-op encrypt --burst-sz 32 --total-ops 30000000 --silent --digest-sz 16 --aead-algo aes-gcm --aead-iv-sz 12</code>
Notes	Use multi-cores configuration for testing is aim to reach maximum of QAT capability

### Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	3.79	3.77	3.24
128	7.50	7.46	6.45
256	14.62	14.47	12.68
512	27.47	27.16	24.07
1024	43.59	43.69	39.11
2048	51.29	51.62	49.23



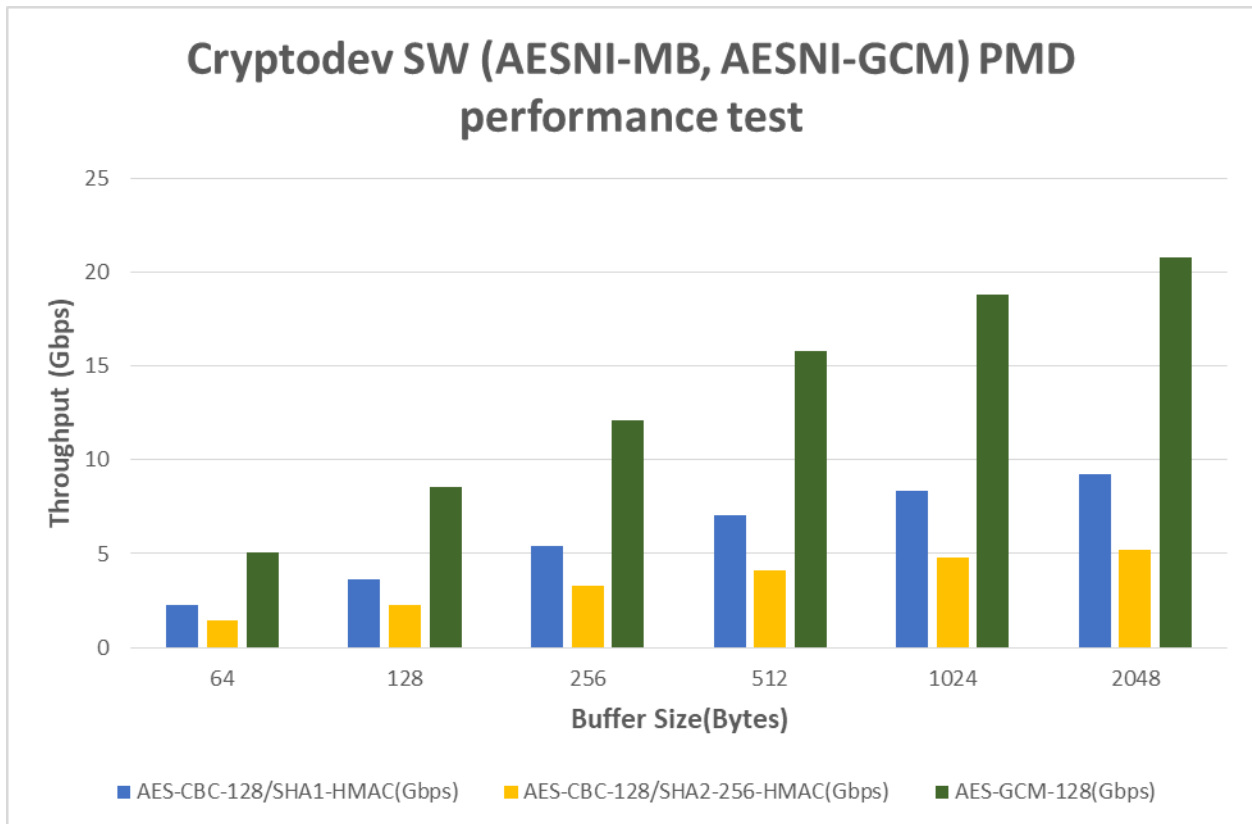
## Test Case 4 – Cryptodev SW (AESNI-MB, AESNI-GCM) PMD performance test

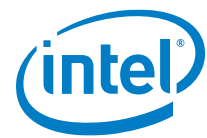
Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC
Cores	1C1T
QAT	Not use
Command line (AES-CBC-128/SHA1-HMAC)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 4,5 -n 4 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 10000000 --silent --digest-sz 12 --auth-algo sha1-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-CBC-128/SHA2-256-HMAC)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 4,5 -n 4 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 10000000 --silent --digest-sz 16 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-GCM-128)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_gcm_pmd_1 -l 4,5 -n 4 -- --aead-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --optype aead --ptest throughput --aead-aad-sz 16 --devtype crypto_aesni_gcm --aead-op encrypt --burst-sz 32 --total-ops 10000000 --silent --digest-sz</code>

	16 --aead-algo aes-gcm --aead-iv-sz 12
Notes	<p>The SW PMD performance is linear scaling out with core numbers. The scale factor is around 1. If the hyper-threading is enabled, extra ~20%-50% performance will be achieved per hyper-thread.</p> <p>Notes: These tests are running with AESNI MB 0.49, since there is a performance issue with AESNI MB 0.48 on this platform.</p>

Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	2.23	1.44	5.07
128	3.66	2.30	8.58
256	5.40	3.27	12.09
512	7.08	4.14	15.82
1024	8.37	4.78	18.82
2048	9.24	5.19	20.81





# Intel Atom® Processor C3958 (16M Cache, 2.00 GHz)

## Hardware & Software Ingredients

Item	Description
Server Platform	Harcuvar
CPU	Intel Atom® Processor C3958 (16M Cache, 2.00 GHz) <a href="https://ark.intel.com/products/series/97941/Intel-Atom-Processor-C-Series">https://ark.intel.com/products/series/97941/Intel-Atom-Processor-C-Series</a> Number of cores 16, Number of threads 16.
Memory	Total 8192 MBs over 2 channels @ 2400 MHz
Operating System	Ubuntu 18.04.3
BIOS	HAVLCRB1.X64.0015.D73.1711010409
Microcode version	0x2e
Linux kernel version	5.0.0-23-generic
GCC version	7.5.0
DPDK version	20.05

### Boot and BIOS settings

Item	Description
Boot settings	intel_iommu=on iommu=pt intel_pstate=disable isolcpus=4-11, nohz_full=4-11 rcu_nocbs=4-11 hugepagesz=1G hugepages=8 default_hugepagesz=1G
BIOS	Energy Performance Bias <Performance> CPU C state Disabled Turbo Disabled
DPDK Settings	Build Options: config/common_base CONFIG_RTE_LIBRTE_PMD_QAT_SYM=y CONFIG_RTE_LIBRTE_PMD_AESNI_MB=y CONFIG_RTE_LIBRTE_PMD_AESNI_GCM=y



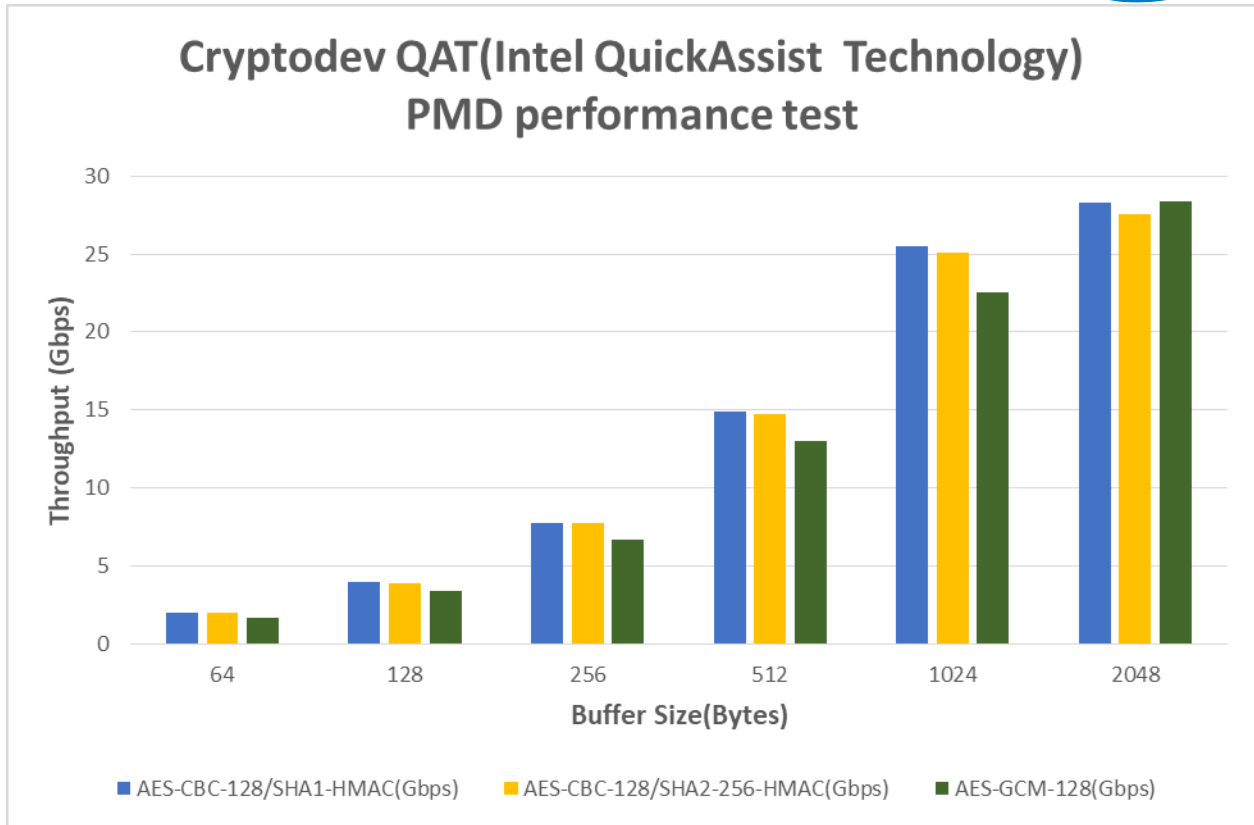
## Test Case 5 – Cryptodev QAT(Intel QuickAssist Technology) PMD performance test

Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC by Intel QuickAssist Technology
Cores	4C4T
QAT	Integrated Intel QuickAssist Technology
Command line (AES-CBC-128/SHA1-HMAC)	<code>./x86_64-native-linuxapp-gcc/build/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -w 0000:01:01.0 -w 0000:01:01.1 -w 0000:01:01.2 -w 0000:01:01.3 -l 6,7,8,9,10 -n 2 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_qat --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 30000000 --silent --digest-sz 20 --auth-algo sha1-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-CBC-128/SHA2-256-HMAC)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -w 0000:01:01.0 -w 0000:01:01.1 -w 0000:01:01.2 -w 0000:01:01.3 -l 6,7,8,9,10 -n 2 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_qat --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 30000000 --silent --digest-sz 32 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-GCM-128)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -w 0000:01:01.0 -w 0000:01:01.1 -w 0000:01:01.2 -w 0000:01:01.3 -l 6,7,8,9,10 -n 2 -- --aead-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --optype aead --ptest throughput --aead-aad-sz 16 --devtype crypto_qat --aead-op encrypt --burst-sz 32 --total-ops 30000000 --silent --digest-sz 16 --aead-algo aes-gcm --aead-iv-sz 12</code>
Notes	Use multi-cores configuration for testing is aim to reach maximum of QAT capability

### Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	1.98	1.98	1.70
128	3.94	3.92	3.39
256	7.77	7.71	6.72
512	14.88	14.76	13.02
1024	25.53	25.06	22.52
2048	28.29	27.53	28.33





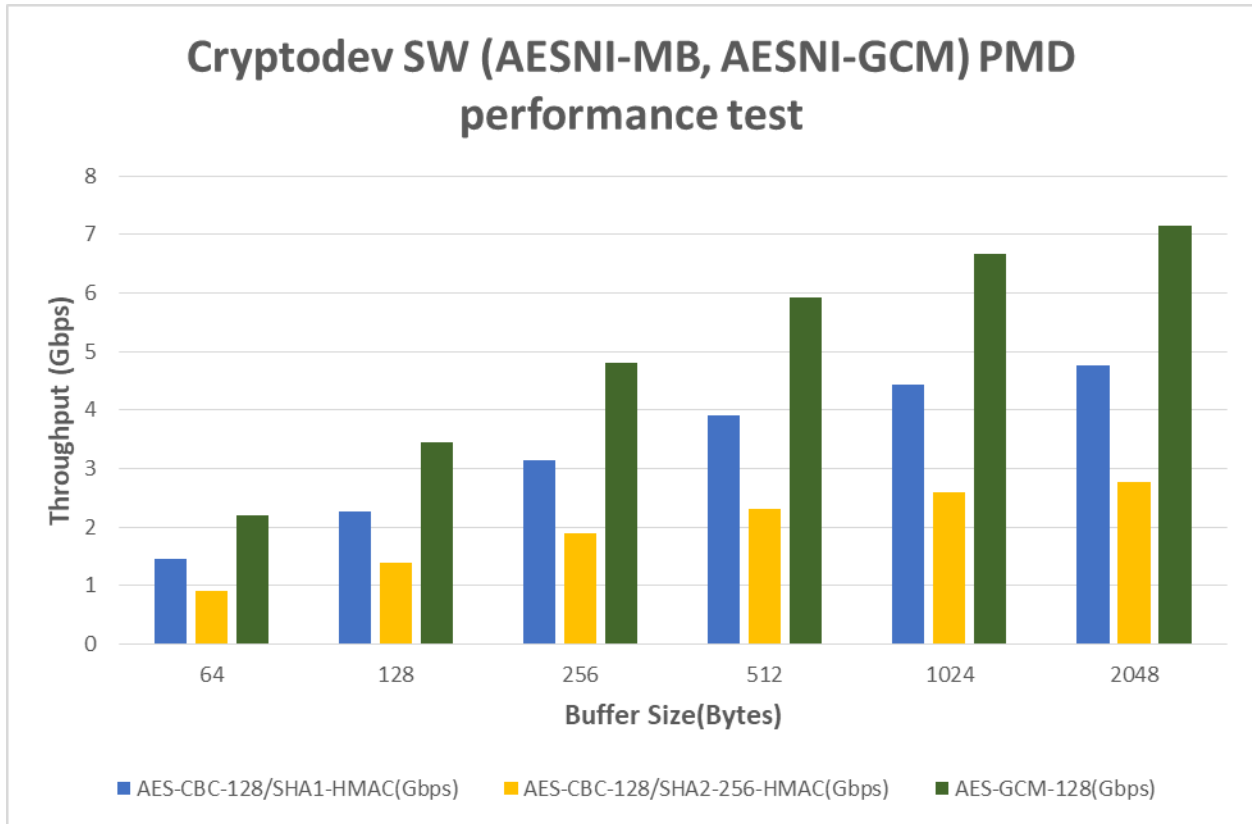


## Test Case 6 – Cryptodev SW (AESNI-MB, AESNI-GCM) PMD performance test

Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC
Cores	1C1T
QAT	Not use
Command line (AES-CBC-128/SHA1-HMAC)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 -w 0000:01:01.0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 6,7 -n 2 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate -burst-sz 32 --total-ops 10000000 --silent --digest-sz 12 --auth-algo sha1-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-CBC-128/SHA2-256-HMAC)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 -w 0000:01:01.0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 6,7 -n 2 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate -burst-sz 32 --total-ops 10000000 --silent --digest-sz 16 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-GCM-128)	<code>./x86_64-native-linuxapp-gcc/build/app/test-crypto-perf/dpdk-test-crypto-perf --socket-mem 2048,0 -w 0000:01:01.0 --legacy-mem --vdev crypto_aesni_gcm_pmd_1 -l 6,7 -n 2 -- --aead-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --optype aead --ptest throughput --aead-aad-sz 16 --devtype crypto_aesni_gcm --aead-op encrypt --burst-sz 32 --total-ops 10000000 --silent --digest-sz 16 --aead-algo aes-gcm --aead-iv-sz 12</code>
Notes	The SW PMD performance is linear scaling out with core numbers. The scale factor is around 1.

### Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	1.46	0.90	2.19
128	2.27	1.38	3.46
256	3.13	1.88	4.80
512	3.90	2.30	5.92
1024	4.43	2.59	6.68
2048	4.76	2.77	7.16



## IPSec Test setup:

---

The device under test (DUT) consists of a system with an Intel® architecture motherboard populated with the following;

- A single or dual processor and PCH chip, except for System on Chip (SoC) cases
- DRAM memory size and frequency (normally single DIMM per channel)
- Specific Intel Network Interface Cards (NICs)
- BIOS settings noting those that updated from the basic settings
- DPDK build configuration settings, and commands used for tests

Benchmarking a DPDK system requires knowledge of networking technologies including knowledge of network protocols and hands-on experience with relevant open-source software, such as Linux\*, and the DPDK. Engineers also need benchmarking and debugging skills, as well as a good understanding of the device-under-test (DUT) across compute and networking domains.

**DPDK ipsec-secgw Test Case:** Documentation may be found at [https://doc.dpdk.org/guides/sample\\_app\\_ug/ipsec\\_secgw.html](https://doc.dpdk.org/guides/sample_app_ug/ipsec_secgw.html).

The application demonstrates the use of IPSec library in the DPDK to implement an IPSec gateway. The gateway could establish an IPSec tunnel between two nodes to provide a security transport layer.

Below is an example setup topology for the performance test. Generally, Cores, memories, Intel QuickAssist Technology hardware are connected to same socket. The performance result for multi-core testing sums each core's throughput number.

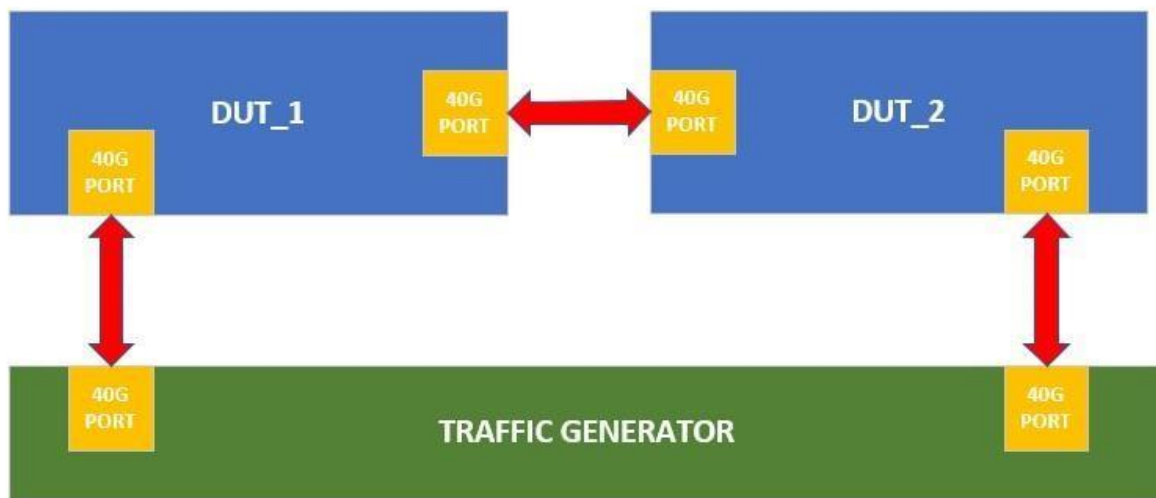


Figure1. DPDK IPSec performance test setup

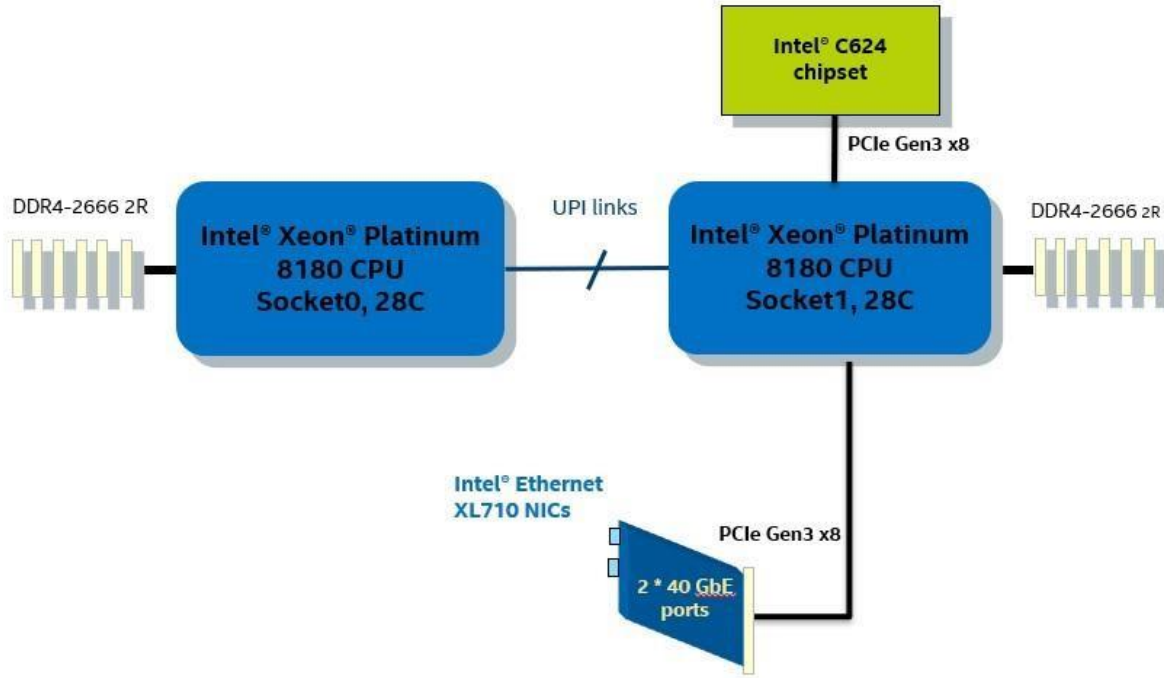
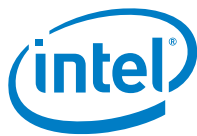


Figure2. Device Under Test Setup (DUT)



# Intel® Xeon® Platinum 8180 Processor (38.5M Cache, 2.50 GHz)

## Hardware & Software Ingredients

Item	Description
Server Platform	Intel® Server Board S2600WFT Intel® Server Board S2600WFT Family
Chipset	<a href="#">Intel® C620 Series Chipset</a>
CPU	Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz <a href="https://ark.intel.com/products/120496/Intel-Xeon-Platinum-8180-Processor38_5M-Cache-2_50-GHz">https://ark.intel.com/products/120496/Intel-Xeon-Platinum-8180-Processor38_5M-Cache-2_50-GHz</a> Number of cores 28/socket, Number of threads 56/socket
Memory	Total 98304 MBs over 12 channels @ 2133 MHz
PCIe	3 x PCIe Gen3 x8 slots
NIC	40GbE XL710
QAT	PCI-e x8 Gen3 mode
Operating System	Ubuntu19.04
BIOS	SE5C620.86B.00.01.0009.101920170742
Microcode version	0x2000064
Linux kernel version	5.0.0-37-generic
GCC version	8.3.0
DPDK version	20.05

### Boot and BIOS settings

Item	Description
Boot settings	intel_iommu=on iommu=pt intel_pstate=disable isolcpus=6-15,22-31 nohz_full=6-15,22-31 rcu_nocbs=6-15,22-31
BIOS	CPU Power and Performance Policy <Performance> CPU C-state Disabled CPU P-state Disabled Enhanced Intel® Speedstep® Tech Disabled Turbo Boost Disabled
DPDK Settings	Build Options: config/common_base CONFIG_RTE_LIBRTE_PMD_QAT_SYM=y CONFIG_RTE_LIBRTE_PMD_AESNI_MB=y CONFIG_RTE_LIBRTE_PMD_AESNI_GCM=y



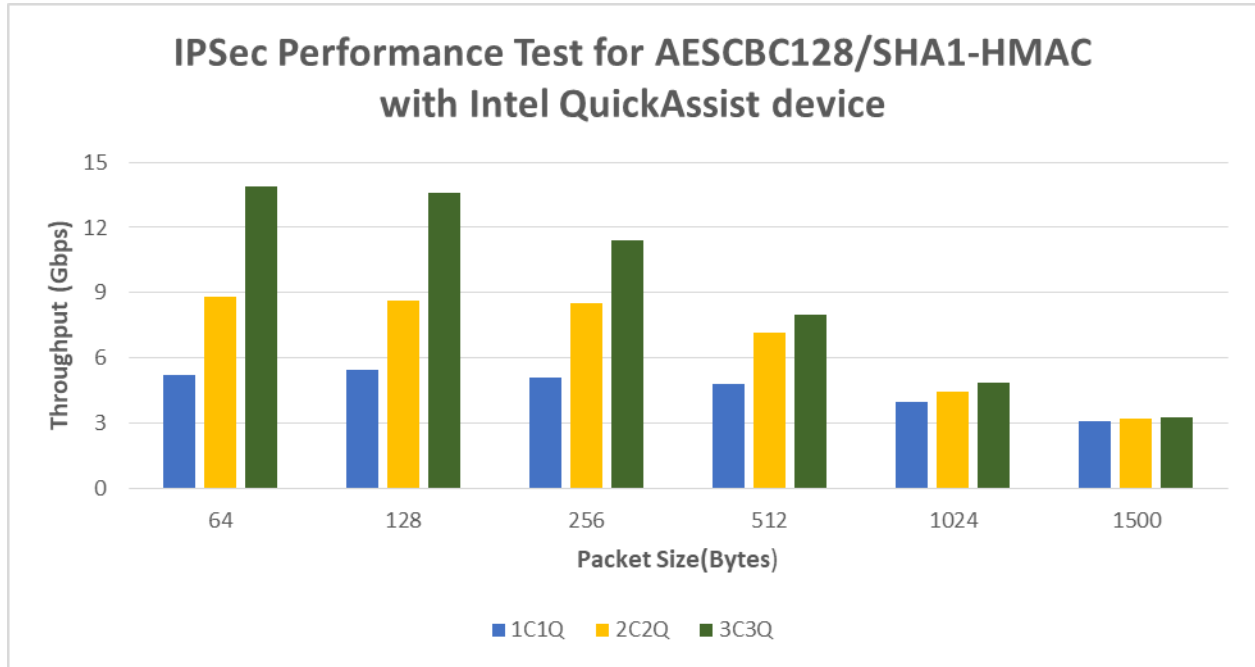
## Test Case 1 – IPsec Performance Test for AESCBC128/SHA1-HMAC with Intel QuickAssist device

Item	Description
Test Case	IPsec Performance Test for AES-CBC-128/SHA1-HMAC with Intel QuickAssist device
Cores (Cores/NIC and QAT Queue)	1C1Q, 2C2Q, 3C3Q
QAT	Integrated Intel QuickAssist Technology , PCI-e x8 Mode
Command Line (AES-CBC128/SHA1HMAC)	<code>./build/ipsec-secgw --lcores=40 -n 4 -w b7:00.0 -w b7:00.1 -w b1:01.0 -- p 0x3 -u 1 -P --config="(0,0,40),(1,0,40)" -f ./ipsec_test_cbc.cfg</code>
Config File (AES-CBC128/SHA1HMAC)	<pre>#SP IPv4 rules sp ipv4 out esp protect 1000 pri 5 dst 11.11.11.2/24 src 11.11.11.1/32 sport 0:65535 dport 0:65535 sp ipv4 out esp protect 1001 pri 5 dst 11.11.12.2/24 src 11.11.11.1/32 sport 0:65535 dport 0:65535 sp ipv4 out esp protect 1002 pri 5 dst 11.11.13.2/24 src 11.11.11.1/32 sport 0:65535 dport 0:65535 sp ipv4 out esp bypass pri 1 sport 0:65535 dport 0:65535  sp ipv4 in esp protect 1010 pri 5 dst 12.12.12.1/32 src 12.12.12.2/32 sport 0:65535 dport 0:65535 sp ipv4 in esp protect 1011 pri 5 dst 12.12.12.1/32 src 12.12.12.3/32 sport 0:65535 dport 0:65535 sp ipv4 in esp protect 1012 pri 5 dst 12.12.12.1/32 src 12.12.12.14/32 sport 0:65535 dport 0:65535 sp ipv4 in esp bypass pri 1 sport 0:65535 dport 0:65535  #SA rules sa out 1000 cipher_algo aes-128-cbc cipher_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef auth_algo sha1-hmac auth_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4-tunnel src 12.12.12.1 dst 12.12.12.2 sa out 1001 cipher_algo aes- 128-cbc cipher_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef auth_algo sha1-hmac auth_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4- tunnel src 12.12.12.1 dst 12.12.12.3 sa out 1002 cipher_algo aes-128-cbc cipher_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef auth_algo sha1-hmac auth_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4- tunnel src 12.12.12.1 dst 12.12.12.14  sa in 1010 cipher_algo aes-128-cbc cipher_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef auth_algo sha1-hmac auth_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4-tunnel src 12.12.12.2 dst 12.12.12.1 sa in 1011 cipher_algo aes-128cbc cipher_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef auth_algo sha1-hmac auth_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4-tunnel src 12.12.12.3 dst 12.12.12.1 sa in 1012 cipher_algo aes-128cbc cipher_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef auth_algo sha1-hmac auth_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4-tunnel src 12.12.12.14 dst 12.12.12.1  #Routing rules rt ipv4 dst 12.12.12.2/24 port 0 rt ipv4 dst 13.13.13.2/8 port 1  neigh port 0 1a:2b:3c:4d:5e:6f</pre>



Test Result (Mpackets /s):

AES-CBC-128	64	128	256	512	1024	1500
1C1Q	5.214	5.487	5.097	4.836	4.012	3.121
2C2Q	8.793	8.642	8.516	7.141	4.472	3.233
3C3Q	13.894	13.574	11.412	7.981	4.852	3.288







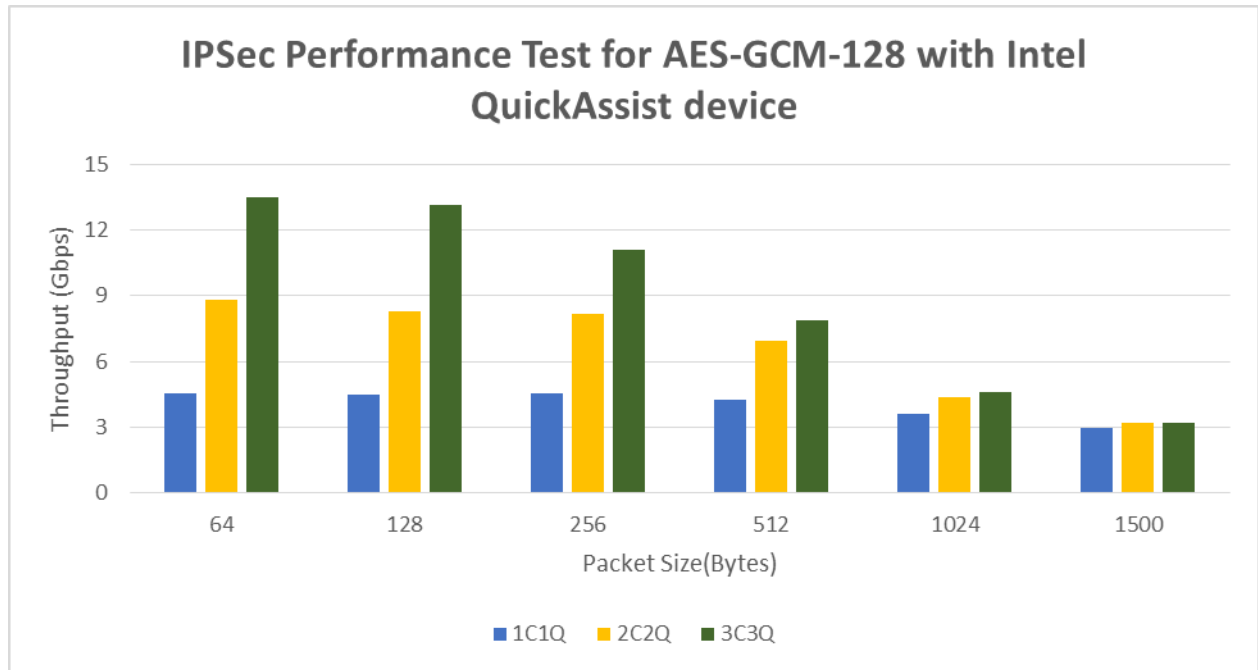
## Test Case 2 – IPsec Performance Test for AES-GCM-128 with Intel QuickAssist device

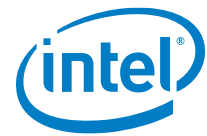
Item	Description
Test Case	IPsec Performance Test for AES-GCM-128 with Intel QuickAssist device
Cores (Cores/NIC and QAT Queue)	1C1Q, 2C2Q, 3C3Q
QAT (Cores/NIC and QAT Queue)	Integrated Intel QuickAssist Technology , PCI-e x8 Mode
Command Line (AES-GCM-128)	<code>./build/ipsec-secgw --lcores=40 -n 4 -w b7:00.0 -w b7:00.1 -w b1:01.0 -- p 0x3 -u 1 -P --config="(0,0,40),(1,0,40)" -f ./ipsec_test_gcm.cfg</code>
Config File (AES-GCM-128)	<pre>#SP IPv4 rules sp ipv4 out esp protect 1000 pri 5 dst 11.11.11.2/24 src 11.11.11.1/32 sport 0:65535 dport 0:65535 sp ipv4 out esp protect 1001 pri 5 dst 11.11.12.2/24 src 11.11.11.1/32 sport 0:65535 dport 0:65535 sp ipv4 out esp protect 1002 pri 5 dst 11.11.13.2/24 src 11.11.11.1/32 sport 0:65535 dport 0:65535 sp ipv4 out esp bypass pri 1 sport 0:65535 dport 0:65535   sp ipv4 in esp protect 1010 pri 5 dst 12.12.12.1/32 src 12.12.12.2/32 sport 0:65535 dport 0:65535 sp ipv4 in esp protect 1011 pri 5 dst 12.12.12.1/32 src 12.12.12.3/32 sport 0:65535 dport 0:65535 sp ipv4 in esp protect 1012 pri 5 dst 12.12.12.1/32 src 12.12.12.14/32 sport 0:65535 dport 0:65535 sp ipv4 in esp bypass pri 1 sport 0:65535 dport 0:65535 #SA rules sa out 1000 aead_algo aes-128gcm aead_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4tunnel src 12.12.12.1 dst 12.12.12.2 sa out 1001 aead_algo aes- 128-gcm aead_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4tunnel src 12.12.12.1 dst 12.12.12.3 sa out 1002 aead_algo aes- 128-gcm aead_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4tunnel src 12.12.12.1 dst 12.12.12.14 sa in 1010 aead_algo aes-128-gcm aead_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4tunnel src 12.12.12.2 dst 12.12.12.1 sa in 1011 aead_algo aes-128gcm aead_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4tunnel src 12.12.12.3 dst 12.12.12.1 sa in 1012 aead_algo aes-128gcm aead_key de:ad:be:ef:de:ad:be:ef:de:ad:be:ef:de:ad:be:ef mode ipv4tunnel src 12.12.12.14 dst 12.12.12.1 #Routing rules rt ipv4 dst 12.12.12.2/24 port 0 rt ipv4 dst 13.13.13.2/8 port 1 neigh port 0 1a:2b:3c:4d:5e:6f</pre>



Test Result: (Mpackets /s)

AES-GCM-128	64	128	256	512	1024	1500
1C1Q	4.569	4.463	4.521	4.221	3.622	2.972
2C2Q	8.813	8.297	8.138	6.924	4.387	3.221
3C3Q	13.492	13.121	11.098	7.864	4.587	3.221



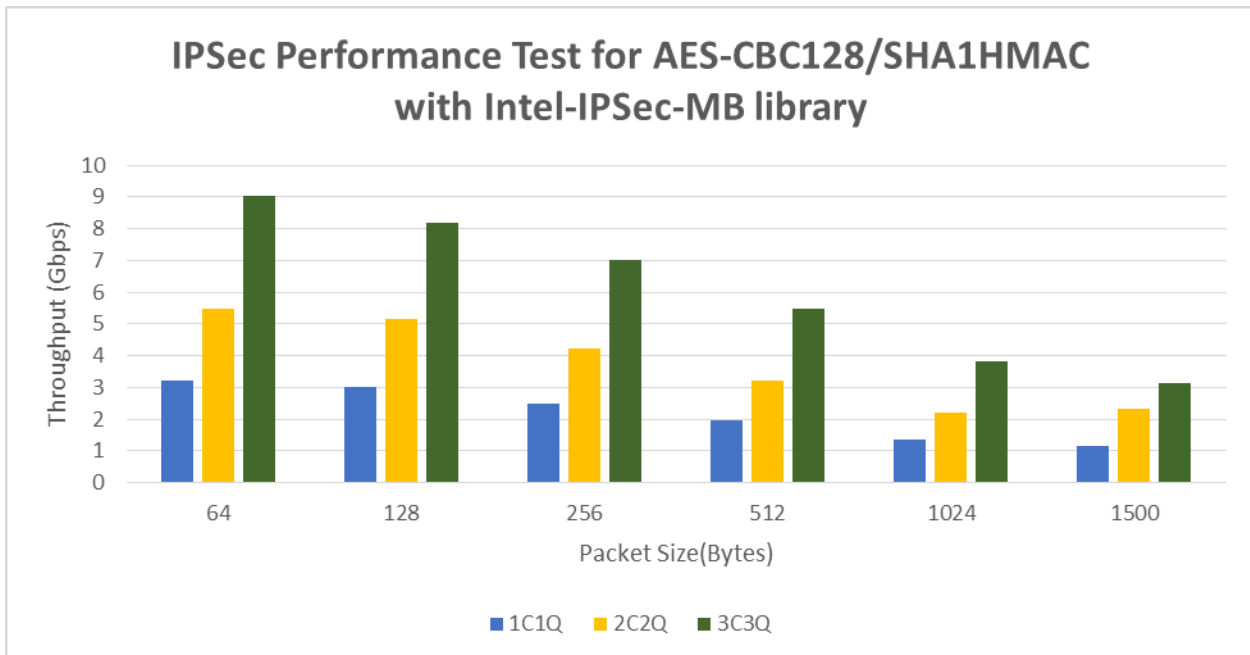


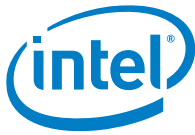
## Test Case 3 – IPsec Performance Test for AES-CBC128/SHA1HMAC with Intel-IPSec-MB library

Item	Description
Test Case	IPsec Performance Test for AES-CBC-128 SHA1-HMAC with Intel-IPSec-MB library (Multi Buffer Library)
IPSec-MB version	0.53
Cores	1C1Q, 2C2Q, 3C3Q
QAT	Not Used
Command Line (AES-GCM-128)	<code>./build/ipsec-secgw --lcores=40 -n 4 -w b7:00.0 -w b7:00.1 vdev="crypto_aesni_mb0,socket_id=1" -- -p 0x3 -u 1 -P config="(0,0,40),(1,0,40)" -f ./ipsec_test_cbc.cfg</code>

### Test Result: (Mpackets /s)

AES-CBC-128	64	128	256	512	1024	1500
1C1Q	3.232	3.002	2.502	1.974	1.367	1.161
2C2Q	5.494	5.156	4.233	3.231	2.198	2.311
3C3Q	9.012	8.176	7.021	5.467	3.819	3.125



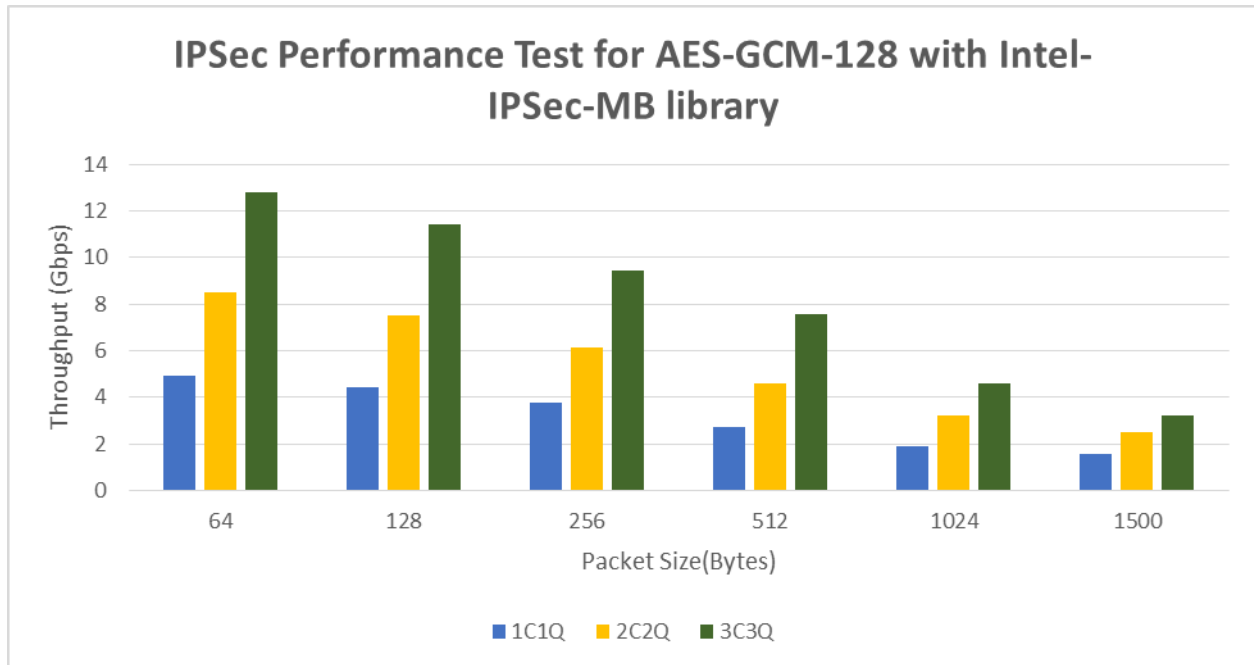


## Test Case 4 – IPsec Performance Test for AES-GCM-128 with Intel-IPsec-MB library

Item	Description
Test Case	IPsec Performance Test for AES-GCM-128 with Intel-IPsec-MB library
IPsec-MB version	0.53
Cores	1C1Q, 2C2Q, 3C3Q
QAT	Not Used
Command Line (AES-GCM-128)	<code>./build/ipsec-secgw --lcores=40 -n 4 -w b7:00.0 -w b7:00.1 vdev="crypto_aesni_gcm0,socket_id=1" -- -p 0x3 -u 1 -P config="(0,0,40),(1,0,40)" -f ./ipsec_test_gcm.cfg</code>

### Test Result: (Mpackets /s)

AES-GCM-128	64	128	256	512	1024	1500
1C1Q	4.902	4.412	3.787	2.723	1.921	1.562
2C2Q	8.521	7.498	6.112	4.603	3.208	2.498
3C3Q	12.783	11.424	9.421	7.582	4.599	3.224





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Performance results are based on testing as of June.22 and may not reflect all publicly available security updates. See configuration disclosure for details. No product can be absolutely secure. For more information go to <http://www.intel.com/performance>

Intel® AES-NI requires a computer system with an AES-NI enabled processor, as well as non-Intel software to execute the instructions in the correct sequence. AES-NI is available on select Intel® processors. For availability, consult your reseller or system manufacturer. **For more information, see <http://software.intel.com/en-us/articles/intel-advanced-encryption-standard-instructions-aes-ni/>**

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