

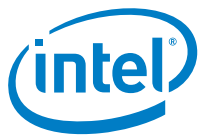
# DPDK Intel Cryptodev Performance Report

## Release 21.11

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**Test Date:** Nov 18<sup>th</sup>, 2021

**Author:** Intel DPDK Validation team



## Revision History

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Date	Revision	Comment
Nov 18 <sup>th</sup> , 2021	1.0	Initial document for release



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## Audience and Purpose

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The primary audience for this test report are architects and engineers implementing the Data Plane Development Kit (DPDK). This report provides information on packet processing performance testing for the specified DPDK release on Intel® architecture. The initial report may be viewed as the baseline for future releases and provides system configuration and test cases based on DPDK examples.

The purpose of reporting these tests is not to imply a single “correct” approach, but rather to provide a baseline of well-tested configurations and procedures with reproducible results. This will help guide architects and engineers who are evaluating and implementing DPDK solutions on Intel® architecture and can assist in achieving optimal system performance.

## Cryptodev Test setup:

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The device under test (DUT) consists of a system with an Intel® architecture motherboard populated with the following;

- A single or dual processor and PCH chip, except for System on Chip (SoC) cases
- DRAM memory size and frequency (normally single DIMM per channel)
- Specific Intel Network Interface Cards (NICs)
- BIOS settings noting those that updated from the basic settings
- DPDK build configuration settings, and commands used for tests

Benchmarking a DPDK system requires knowledge of networking technologies including knowledge of network protocols and hands-on experience with relevant open-source software, such as Linux\*, and the DPDK. Engineers also need benchmarking and debugging skills, as well as a good understanding of the device-under-test (DUT) across compute and networking domains.

**dpdk-test-crypto-perf Application:** Documentation may be found at <http://dpdk.org/doc/guides/tools/cryptoperf.html>.

The dpdk-test-crypto-perf tool is a Data Plane Development Kit (DPDK) utility that allows measuring performance parameters of PMDs available in the crypto tree. There are available for two measurement types: throughput and latency. Users can use multiple cores to run tests on but only one type of crypto PMD can be measured during single application execution. Cipher parameters, type of device, type of operation and chain mode have to be specified in the command line as application parameters. These parameters are checked using device capabilities structure.

Below is an example setup topology for the performance test. Generally, Cores, memories, Intel QuickAssist Technology hardware are connected to same socket. The performance result for multi-core testing sums each core’s throughput number.

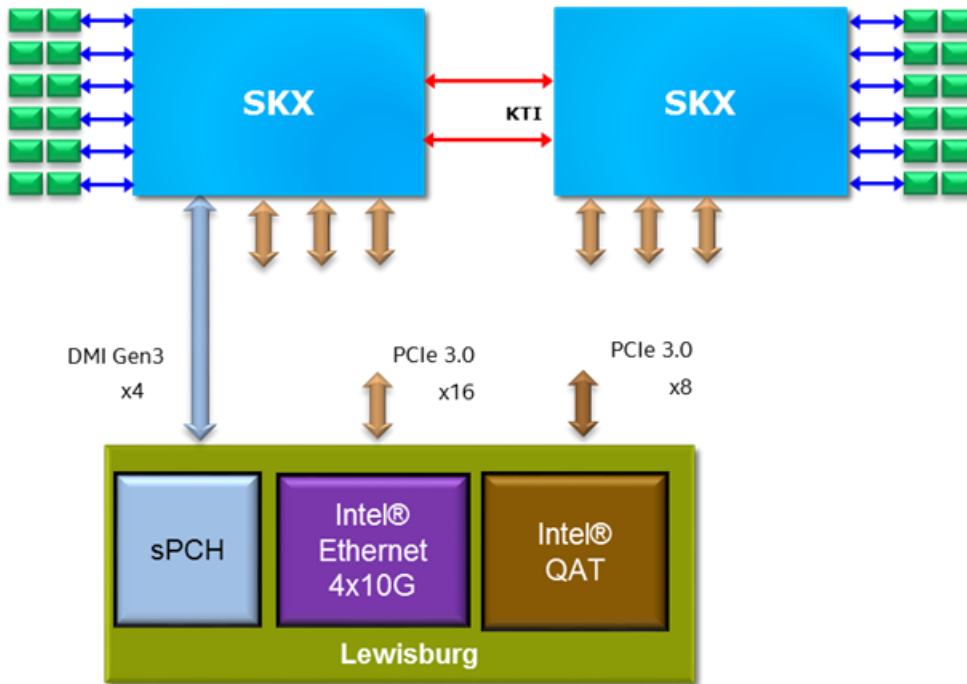


Figure1. DPDK cryptodev performance test setup



# Intel® Xeon® Platinum 8180 Processor (38.5M Cache, 2.50 GHz)

## Hardware & Software Ingredients

Item	Description
Server Platform	PURLEY
Chipset	<a href="#">Intel® C620 Series Chipset</a>
CPU	Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz <a href="https://ark.intel.com/products/120496/Intel-Xeon-Platinum-8180-Processor-38_5M-Cache-2_50-GHz">https://ark.intel.com/products/120496/Intel-Xeon-Platinum-8180-Processor-38_5M-Cache-2_50-GHz</a> Number of cores 28, Number of threads 56.
Memory	Total 98304 MBs over 12 channels @ 2133 MHz
PCIe	3 x PCIe Gen3 x8 slots
QAT	PCI-e x16 mode
Operating System	Ubuntu18.04.5 LTS (Bionic Beaver)
BIOS	SE5C620.86B.00.01.0009.101920170742
Microcode version	0x2006b06
Linux kernel version	5.4.0-81-generic
GCC version	7.5.0
DPDK version	21.11

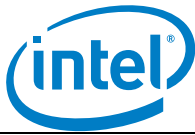
### Boot and BIOS settings

Item	Description
Boot settings	<code>intel_iommu=on iommu=pt intel_pstate=disable isolcpus=4-7,12-15 nohz_full=4-7,12-15 rcu_nocbs=4-7,12-15</code>
BIOS	CPU Power and Performance Policy <Performance> Package C-state Disabled Hardware P-state Disabled Enhanced Intel® Speedstep® Tech Disabled Intel® Turbo Boost Technology Disabled
DPDK Settings	Build Options: config/common_base CONFIG_RTE_LIBRTE_PMD_QAT_SYM=y CONFIG_RTE_LIBRTE_PMD_AESNI_MB=y CONFIG_RTE_LIBRTE_PMD_AESNI_GCM=y



## Test Case 1 – Cryptodev QAT(Intel QuickAssist Technology) PMD performance test

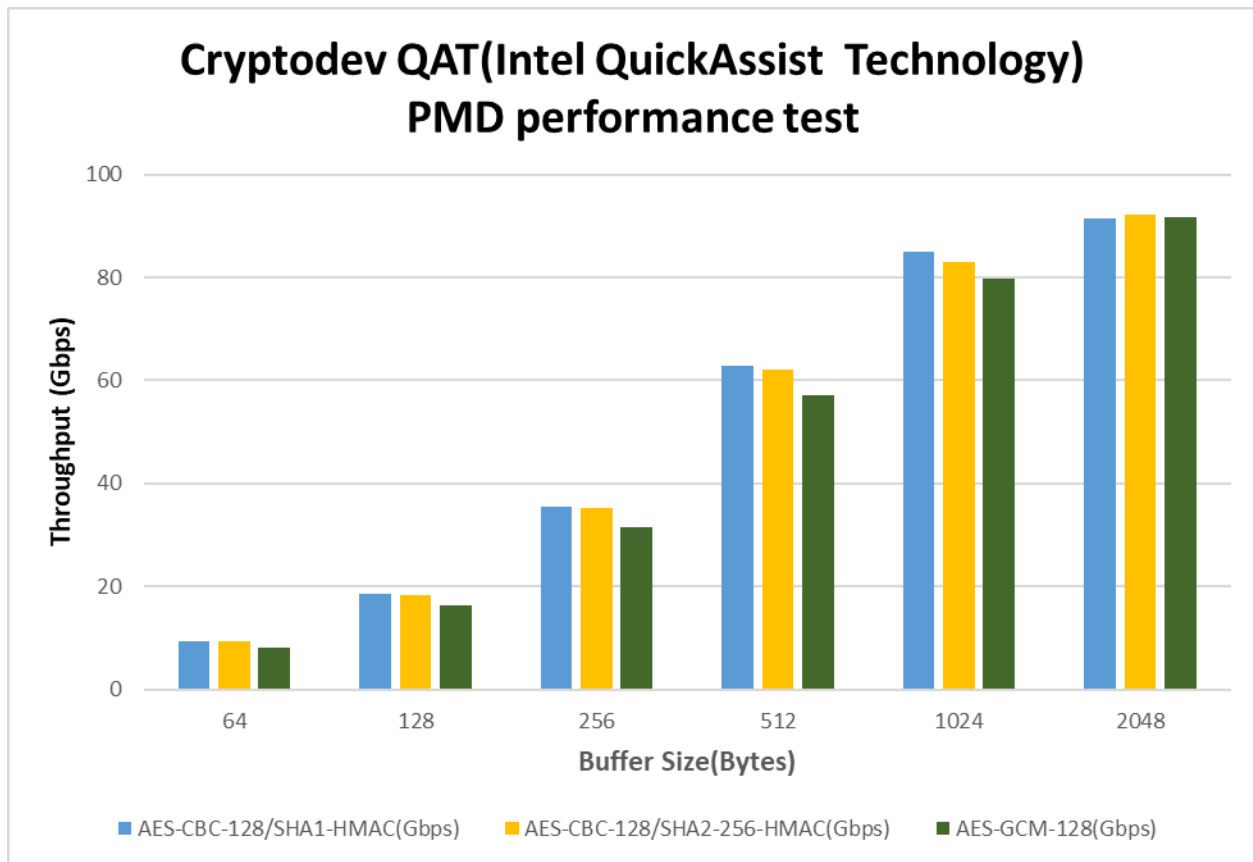
Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC with Intel QuickAssist Technology
Cores	3C6T
QAT	Integrated Intel QuickAssist Technology , PCI-e x16 Mode
Command line (AES-CBC-128/SHA1-HMAC)	<pre>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -a 0000:1a:01.0 -a 0000:1c:01.0 -a 0000:1e:01.0 -a 0000:1a:01.1 -a 0000:1c:01.1 -a 0000:1e:01.1 -a 0000:1a:01.2 -a 0000:1c:01.2 -a 0000:1e:01.2 -a 0000:1a:01.3 -a 0000:1c:01.3 -a 0000:1e:01.3 -a 0000:1a:01.4 -a 0000:1c:01.4 -a 0000:1e:01.4 -a 0000:1a:01.5 -a 0000:1c:01.5 -a 0000:1e:01.5 --vdev crypto_scheduler_pmd_1,worker=0000:1a:01.0_qat_sym,worker=0000:1c:01.0_qat _sym,worker=0000:1e:01.0_qat_sym,mode=round-robin --vdev crypto_scheduler_pmd_2,worker=0000:1a:01.1_qat_sym,worker=0000:1c:01.1_qat _sym,worker=0000:1e:01.1_qat_sym,mode=round-robin --vdev crypto_scheduler_pmd_3,worker=0000:1a:01.2_qat_sym,worker=0000:1c:01.2_qat _sym,worker=0000:1e:01.2_qat_sym,mode=round-robin --vdev crypto_scheduler_pmd_4,worker=0000:1a:01.3_qat_sym,worker=0000:1c:01.3_qat _sym,worker=0000:1e:01.3_qat_sym,mode=round-robin --vdev crypto_scheduler_pmd_5,worker=0000:1a:01.4_qat_sym,worker=0000:1c:01.4_qat _sym,worker=0000:1e:01.4_qat_sym,mode=round-robin --vdev crypto_scheduler_pmd_6,worker=0000:1a:01.5_qat_sym,worker=0000:1c:01.5_qat _sym,worker=0000:1e:01.5_qat_sym,mode=round-robin -l 9,10,66,11,67,12,68 - n 6 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth -- ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_scheduler --cipher-iv-sz 16 --auth-op generate --burst-sz 32 -- total-ops 30000000 --silent --digest-sz 20 --auth-algo sha1-hmac -- cipher-algo aes-cbc --cipher-op encrypt</pre>
Command line (AES-CBC-128/SHA2-256-HMAC)	<pre>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -a 0000:1a:01.0 -a 0000:1c:01.0 -a 0000:1e:01.0 -a 0000:1a:01.1 -a 0000:1c:01.1 -a 0000:1e:01.1 -a 0000:1a:01.2 -a 0000:1c:01.2 -a 0000:1e:01.2 -a 0000:1a:01.3 -a 0000:1c:01.3 -a 0000:1e:01.3 -a 0000:1a:01.4 -a 0000:1c:01.4 -a 0000:1e:01.4 -a 0000:1a:01.5 -a 0000:1c:01.5 -a 0000:1e:01.5 --vdev crypto_scheduler_pmd_1,worker=0000:1a:01.0_qat_sym,worker=0000:1c:01.0_qat _sym,worker=0000:1e:01.0_qat_sym,mode=round-robin -- vdev=crypto_scheduler_pmd_2,worker=0000:1a:01.1_qat_sym,worker=0000:1c:01. 1_qat_sym,worker=0000:1e:01.1_qat_sym,mode=round-robin -- vdev=crypto_scheduler_pmd_3,worker=0000:1a:01.2_qat_sym,worker=0000:1c:01. 2_qat_sym,worker=0000:1e:01.2_qat_sym,mode=round-robin -- vdev=crypto_scheduler_pmd_4,worker=0000:1a:01.3_qat_sym,worker=0000:1c:01. 3_qat_sym,worker=0000:1e:01.3_qat_sym,mode=round-robin -- vdev=crypto_scheduler_pmd_5,worker=0000:1a:01.4_qat_sym,worker=0000:1c:01. 4_qat_sym,worker=0000:1e:01.4_qat_sym,mode=round-robin -- vdev=crypto_scheduler_pmd_6,worker=0000:1a:01.5_qat_sym,worker=0000:1c:01. 5_qat_sym,worker=0000:1e:01.5_qat_sym,mode=round-robin -l 9,10,66,11,67,12,68 -n 6 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 -- devtype crypto_scheduler --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 30000000 --silent --digest-sz 32 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</pre>
Command line (AES-GCM-128)	<pre>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -a 0000:1a:01.0 -a 0000:1c:01.0 -a 0000:1e:01.0 -a 0000:1a:01.1 -a 0000:1c:01.1 -a 0000:1e:01.1 -a 0000:1a:01.2 -a 0000:1c:01.2 -a 0000:1e:01.2 -a 0000:1a:01.3 -a 0000:1c:01.3 -a 0000:1e:01.3 -a 0000:1a:01.4 -a 0000:1c:01.4 -a 0000:1e:01.4 -a 0000:1a:01.5 -a 0000:1c:01.5 -a 0000:1e:01.5 --vdev crypto_scheduler_pmd_1,worker=0000:1a:01.0_qat_sym,worker=0000:1c:01.0_qat _sym,worker=0000:1e:01.0_qat_sym,mode=round-robin -- vdev=crypto_scheduler_pmd_2,worker=0000:1a:01.1_qat_sym,worker=0000:1c:01. 1_qat_sym,worker=0000:1e:01.1_qat_sym,mode=round-robin -- vdev=crypto_scheduler_pmd_3,worker=0000:1a:01.2_qat_sym,worker=0000:1c:01. 2_qat_sym,worker=0000:1e:01.2_qat_sym,mode=round-robin --</pre>



	<pre>vdev=crypto_scheduler_pmd_4,worker=0000:1a:01.3_qat_sym,worker=0000:1c:01.3_qat_sym,worker=0000:1e:01.3_qat_sym,mode=round-robin -- vdev=crypto_scheduler_pmd_5,worker=0000:1a:01.4_qat_sym,worker=0000:1c:01.4_qat_sym,worker=0000:1e:01.4_qat_sym,mode=round-robin -- vdev=crypto_scheduler_pmd_6,worker=0000:1a:01.5_qat_sym,worker=0000:1c:01.5_qat_sym,worker=0000:1e:01.5_qat_sym,mode=round-robin -l 9,10,66,11,67,12,68 -n 6 -- --aead-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --optype aead --ptest throughput --aead-aad-sz 16 --devtype crypto_scheduler --aead-op encrypt --burst-sz 32 --total-ops 30000000 --silent --digest-sz 16 --aead-algo aes-gcm --aead-iv-sz 12</pre>
Notes	Use multi-cores configuration for testing, aim to reach maximum of QAT capability

Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	9.40	9.33	8.22
128	18.51	18.33	16.27
256	35.51	35.24	31.56
512	62.86	62.03	57.23
1024	85.05	83.04	79.71
2048	91.40	92.21	91.63





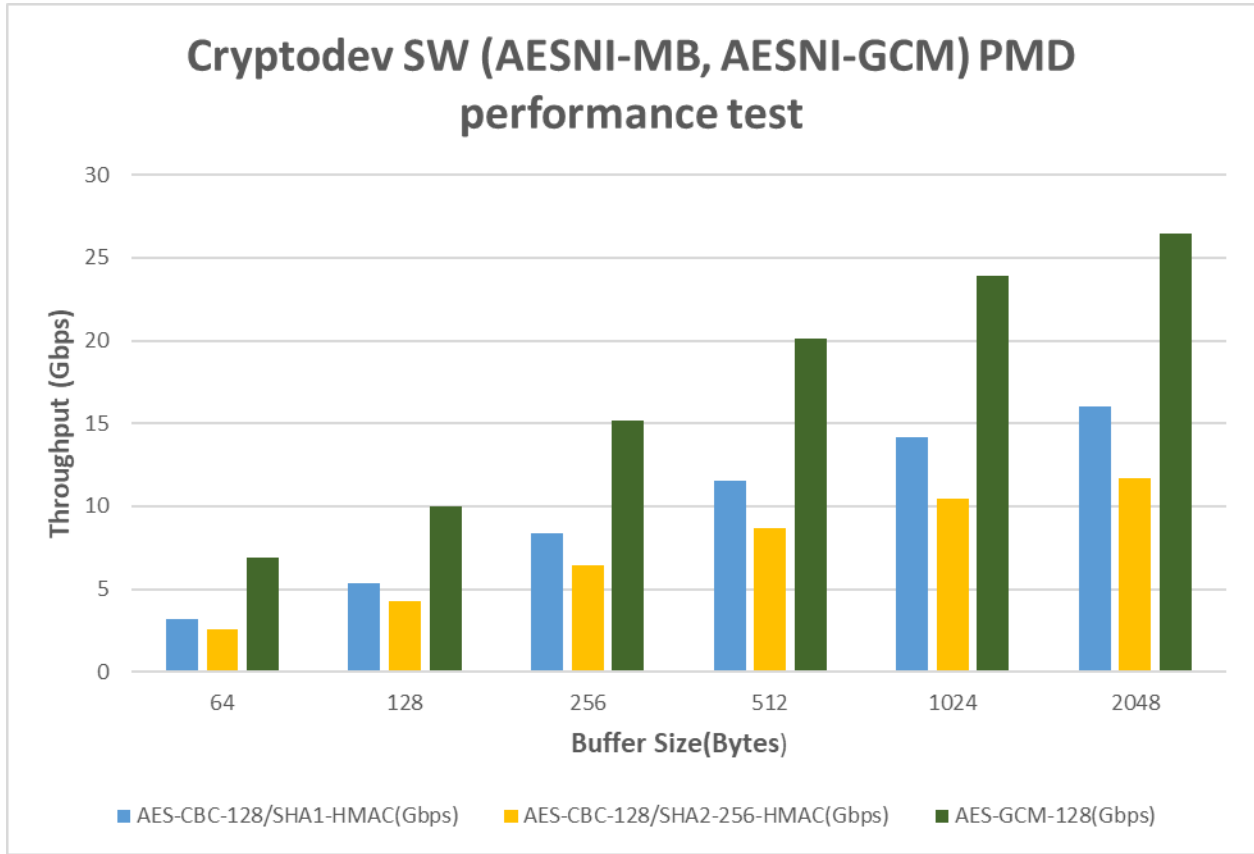


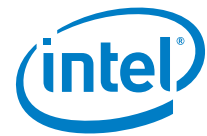
## Test Case 2 – Cryptodev SW (AESNI-MB, AESNI-GCM) PMD performance test

Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC
Cores	1C1T
QAT	Not use
Command line (AES-CBC-128/SHA1-HMAC)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 9,10 -n 6 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 10000000 --silent --digest-sz 12 --auth-algo sha1-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-CBC-128/SHA2-256-HMAC)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 9,10 -n 6 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 10000000 --silent --digest-sz 16 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-GCM-128)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_gcm_pmd_1 -l 9,10 -n 6 -- --aad-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --optype aead --ptest throughput --aad-aad-sz 16 --devtype crypto_aesni_gcm --aad-op encrypt --burst-sz 32 --total-ops 10000000 --silent --digest-sz 16 --aad-algo aes-gcm --aad-iv-sz 12</code>
Notes	<p>The SW PMD performance is linear scaling out with core numbers.</p> <p>The scale factor is around 1. If the hyper-threading is enabled, extra ~20%-50% performance will be achieved per hyper-thread.</p> <p>Notes: These tests are running with AESNI MB 1.0 and AESNI GCM performance dropped (-5%) when avx512 instruction enabled on this platform.</p>

### Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	3.18	2.57	6.89
128	5.36	4.26	10.02
256	8.35	6.47	15.15
512	11.52	8.70	20.11
1024	14.15	10.48	23.92
2048	16.02	11.69	26.43





# Intel® Xeon® Processor D-1553N (12M Cache, 2.30 GHz)

## Hardware & Software Ingredients

Item	Description
Server Platform	GRANGEVILLE
CPU	Intel® Xeon® Processor D-1553N (12M Cache, 2.30 GHz) <a href="https://ark.intel.com/products/123002/Intel-Xeon-Processor-D-1553N-12M-Cache-2_30-GHz">https://ark.intel.com/products/123002/Intel-Xeon-Processor-D-1553N-12M-Cache-2_30-GHz</a> Number of cores 8, Number of threads 16.
Memory	Total 65536 MBs over 4 channels @ 2400 MHz
Operating System	Ubuntu 18.04.5 LTS (Bionic Beaver)
BIOS	GNVDTRL1.86B.0010.D51.1706230411
Microcode version	0xe00000f
Linux kernel version	5.4.0-42-generic
GCC version	7.5.0
DPDK version	21.11

### Boot and BIOS settings

Item	Description
Boot settings	intel_iommu=on iommu=pt intel_pstate=disable isolcpus=4-7,12-15 nohz_full=4-7,12-15 rcu_nocbs=4-7,12-15 hugepagesz=1G hugepages=10 default_hugepagesz=1G
BIOS	Boot performance mode <Max Performance> CPU C state Disabled Energy efficient P-state Disabled Turbo Mode Disabled
DPDK Settings	Build Options: config/common_base CONFIG_RTE_LIBRTE_PMD_QAT_SYM=y CONFIG_RTE_LIBRTE_PMD_AESNI_MB=y CONFIG_RTE_LIBRTE_PMD_AESNI_GCM=y

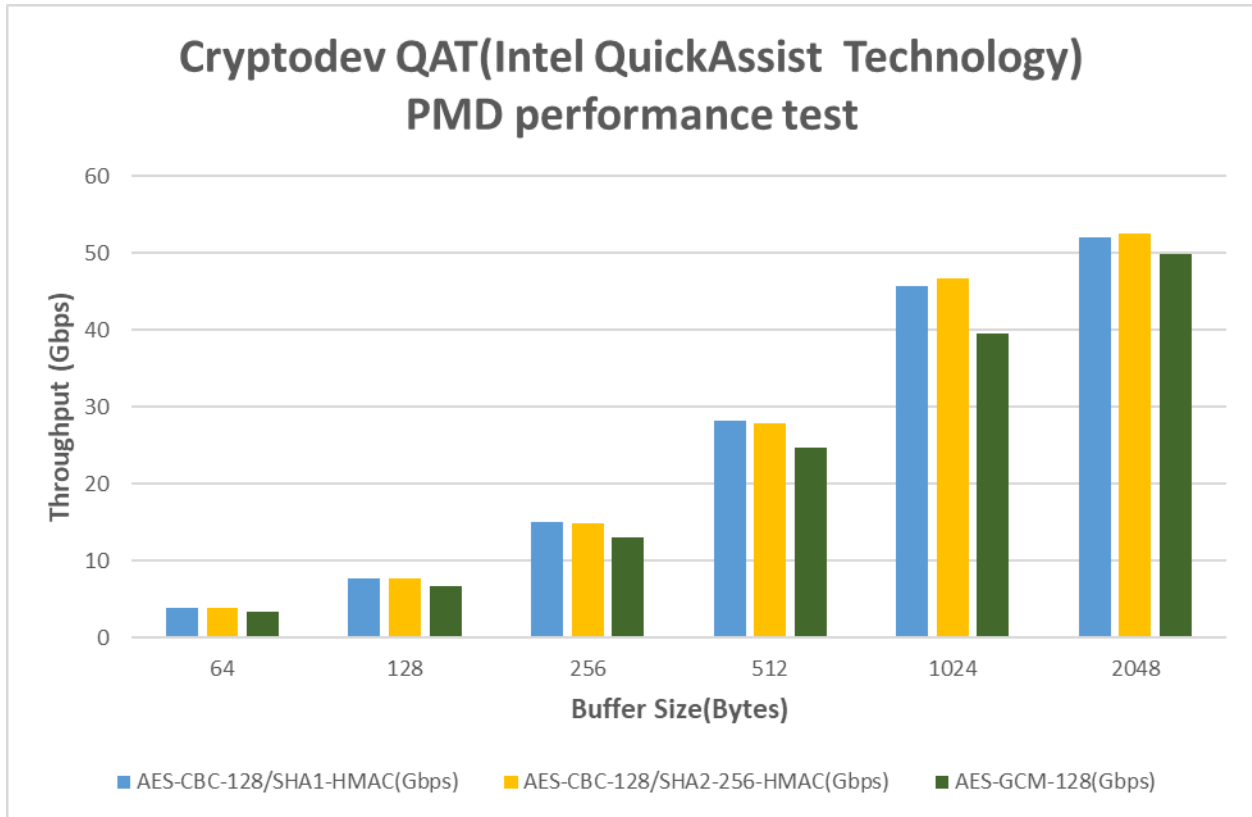
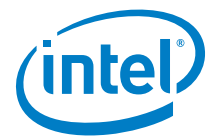


## Test Case 3 – Cryptodev QAT(Intel QuickAssist Technology) PMD performance test

Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC by Intel QuickAssist Technology
Cores	2C4T
QAT	Integrated Intel QuickAssist Technology
Command line (AES-CBC-128/SHA1-HMAC)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -a 0000:02:01.0 -a 0000:02:01.1 -a 0000:02:01.2 -a 0000:02:01.3 -l 4,5,13,6,14 -n 4 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_qat --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 30000000 --silent --digest-sz 20 --auth-algo sha1-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-CBC-128/SHA2-256-HMAC)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -a 0000:02:01.0 -a 0000:02:01.1 -a 0000:02:01.2 -a 0000:02:01.3 -l 4,5,13,6,14 -n 4 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_qat --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 30000000 --silent --digest-sz 32 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-GCM-128)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -a 0000:02:01.0 -a 0000:02:01.1 -a 0000:02:01.2 -a 0000:02:01.3 -l 4,5,13,6,14 -n 4 -- --aead-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --optype aead --ptest throughput --aead-aad-sz 16 --devtype crypto_qat --aead-op encrypt --burst-sz 32 --total-ops 30000000 --silent --digest-sz 16 --aead-algo aes-gcm --aead-iv-sz 12</code>
Notes	Use multi-cores configuration for testing is aim to reach maximum of QAT capability

### Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	3.90	3.89	3.34
128	7.73	7.69	6.65
256	15.08	14.94	13.09
512	28.23	27.91	24.64
1024	45.77	46.77	39.53
2048	52.04	52.54	49.83



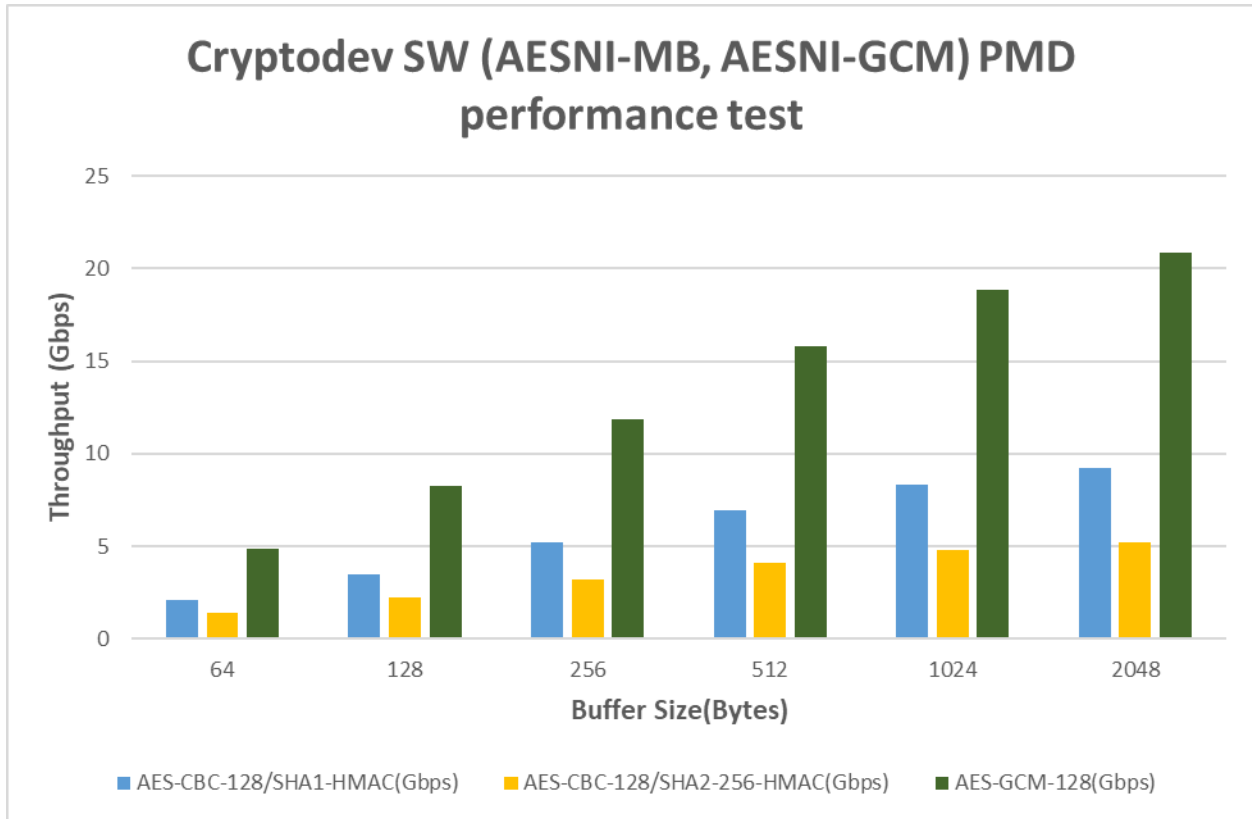
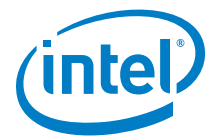


## Test Case 4 – Cryptodev SW (AESNI-MB, AESNI-GCM) PMD performance test

Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC
Cores	1C1T
QAT	Not use
Command line (AES-CBC-128/SHA1-HMAC)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 4,5 -n 4 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 10000000 --silent --digest-sz 12 --auth-algo sha1-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-CBC-128/SHA2-256-HMAC)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 4,5 -n 4 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 10000000 --silent --digest-sz 16 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-GCM-128)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_gcm_pmd_1 -l 4,5 -n 4 -- --aad-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --optype aead --ptest throughput --aad-aad-sz 16 --devtype crypto_aesni_gcm --aad-op encrypt --burst-sz 32 --total-ops 10000000 --silent --digest-sz 16 --aad-algo aes-gcm --aad-iv-sz 12</code>
Notes	<p>The SW PMD performance is linear scaling out with core numbers.</p> <p>The scale factor is around 1. If the hyper-threading is enabled, extra ~20%-50% performance will be achieved per hyper-thread.</p> <p>Notes: These tests are running with AESNI MB 1.0, since there is a performance issue with AESNI MB 0.48 on this platform.</p>

### Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	2.09	1.41	4.88
128	3.49	2.26	8.24
256	5.21	3.23	11.89
512	6.93	4.11	15.77
1024	8.30	4.78	18.86
2048	9.22	5.19	20.86





# Intel Atom® Processor C3958 (16M Cache, 2.00 GHz)

## Hardware & Software Ingredients

Item	Description
Server Platform	Harcuvar
CPU	Intel Atom® Processor C3958 (16M Cache, 2.00 GHz) <a href="https://ark.intel.com/products/series/97941/Intel-Atom-Processor-C-Series">https://ark.intel.com/products/series/97941/Intel-Atom-Processor-C-Series</a> Number of cores 16, Number of threads 16.
Memory	Total 8192 MBs over 2 channels @ 2400 MHz
Operating System	Ubuntu 18.04.3
BIOS	HAVLCRB1.X64.0015.D73.1711010409
Microcode version	0x2e
Linux kernel version	5.0.0-23-generic
GCC version	7.5.0
DPDK version	21.11

### Boot and BIOS settings

Item	Description
Boot settings	intel_iommu=on iommu=pt intel_pstate=disable isolcpus=4-11, nohz_full=4-11 rcu_nocbs=4-11 hugepagesz=1G hugepages=8 default_hugepagesz=1G
BIOS	Energy Performance Bias <Performance> CPU C state Disabled Turbo Disabled
DPDK Settings	Build Options: config/common_base CONFIG_RTE_LIBRTE_PMD_QAT_SYM=y CONFIG_RTE_LIBRTE_PMD_AESNI_MB=y CONFIG_RTE_LIBRTE_PMD_AESNI_GCM=y



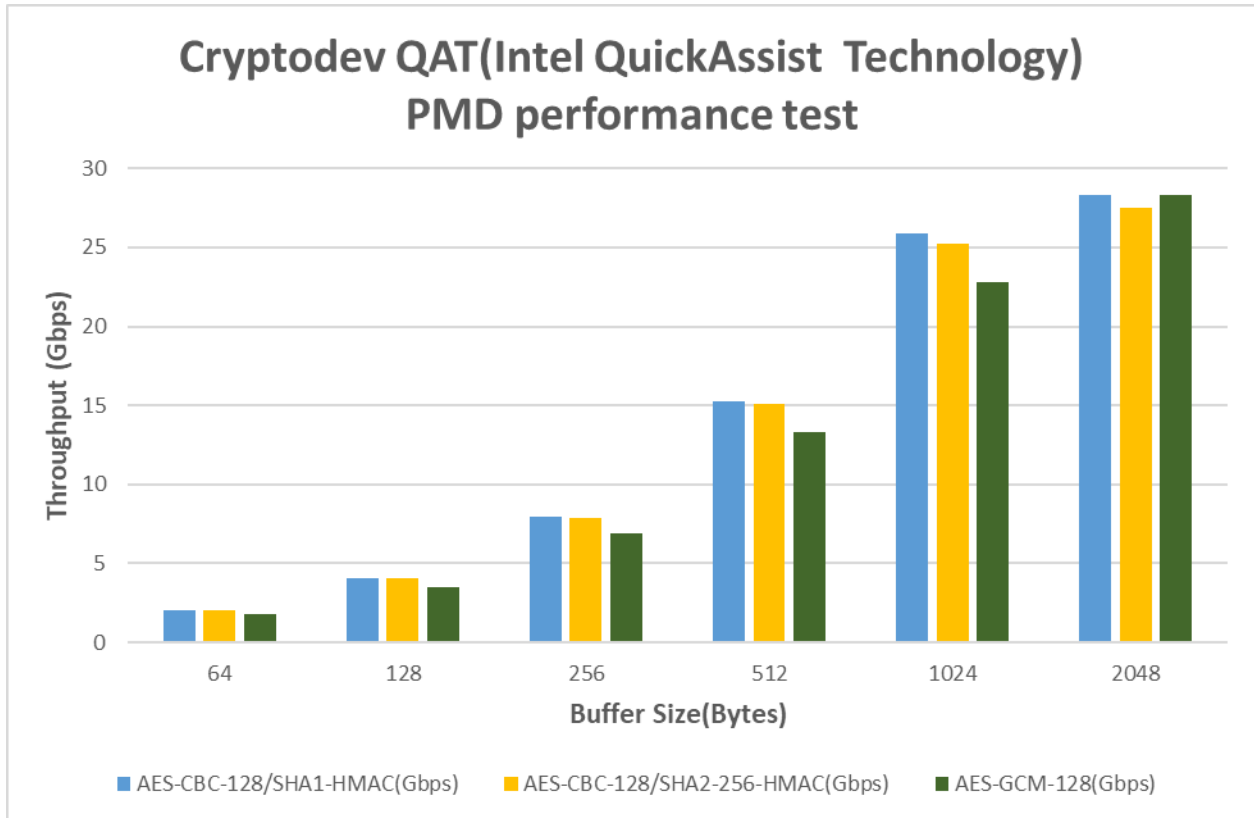


## Test Case 5 – Cryptodev QAT(Intel QuickAssist Technology) PMD performance test

Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC by Intel QuickAssist Technology
Cores	4C4T
QAT	Integrated Intel QuickAssist Technology
Command line (AES-CBC-128/SHA1-HMAC)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -a 0000:01:01.0 -a 0000:01:01.1 -a 0000:01:01.2 -a 0000:01:01.3 -l 6,7,8,9,10 -n 2 -- --buffer-sz 64,128,256,512,1024,2048 -o optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_qat --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 30000000 --silent --digest-sz 20 --auth-algo sha1-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-CBC-128/SHA2-256-HMAC)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -a 0000:01:01.0 -a 0000:01:01.1 -a 0000:01:01.2 -a 0000:01:01.3 -l 6,7,8,9,10 -n 2 -- --buffer-sz 64,128,256,512,1024,2048 -o optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_qat --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 30000000 --silent --digest-sz 32 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-GCM-128)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem -a 0000:01:01.0 -a 0000:01:01.1 -a 0000:01:01.2 -a 0000:01:01.3 -l 6,7,8,9,10 -n 2 -- --aead-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --o type aead --ptest throughput --aead-aad-sz 16 --devtype crypto_qat --aead-op encrypt --burst-sz 32 --total-ops 30000000 --silent --digest-sz 16 --aead-algo aes-gcm --aead-iv-sz 12</code>
Notes	Use multi-cores configuration for testing is aim to reach maximum of QAT capability

### Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	2.03	2.03	1.75
128	4.04	4.03	3.48
256	7.97	7.91	6.90
512	15.23	15.10	13.33
1024	25.88	25.24	22.83
2048	28.28	27.53	28.34



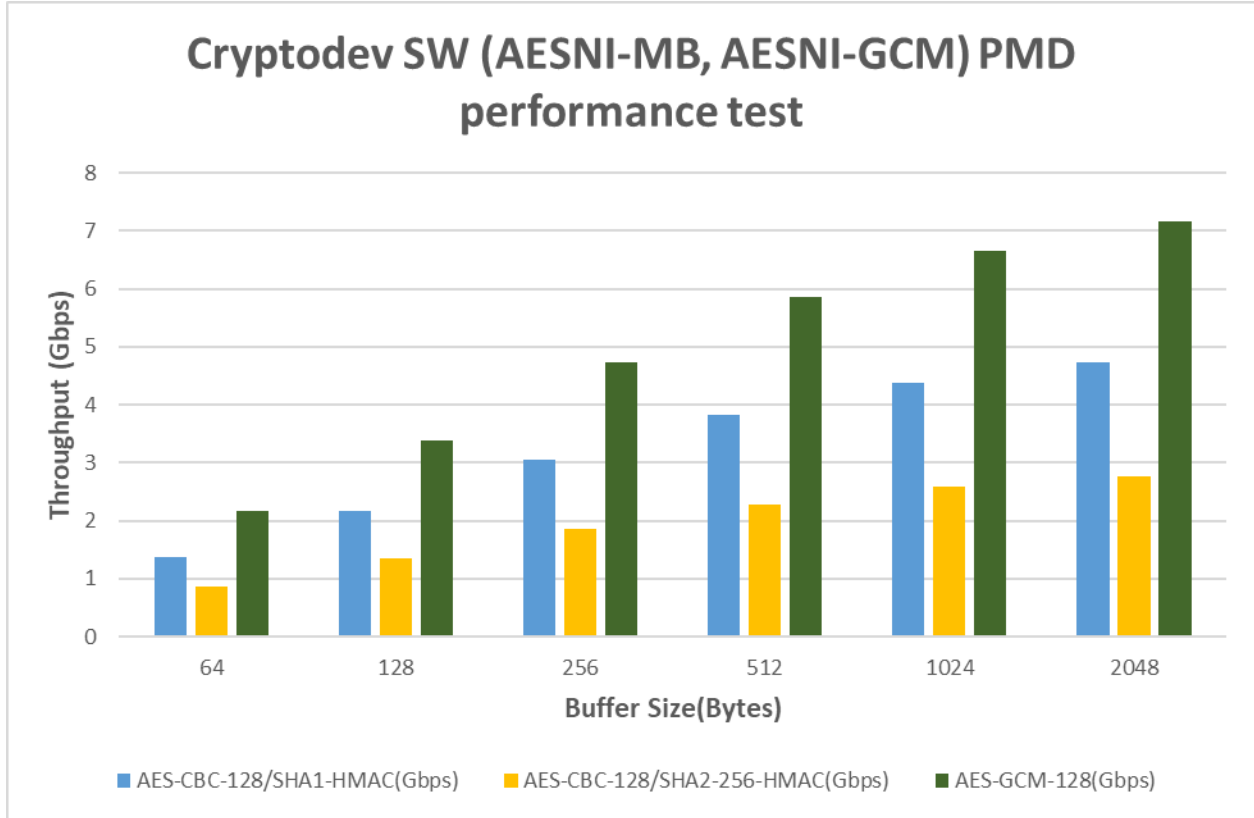


## Test Case 6 – Cryptodev SW (AESNI-MB, AESNI-GCM) PMD performance test

Item	Description
Test Case	Cryptodev performance for AES-CBC-128/SHA1-HMAC, AES-GCM-128, AES-CBC-128/SHA2-256-HMAC
Cores	1C1T
QAT	Not use
Command line (AES-CBC-128/SHA1-HMAC)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 6,7 -n 2 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 10000000 --silent --digest-sz 12 --auth-algo sha1-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-CBC-128/SHA2-256-HMAC)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_mb_pmd_1 -l 6,7 -n 2 -- --buffer-sz 64,128,256,512,1024,2048 --optype cipher-then-auth --ptest throughput --auth-key-sz 64 --cipher-key-sz 16 --devtype crypto_aesni_mb --cipher-iv-sz 16 --auth-op generate --burst-sz 32 --total-ops 10000000 --silent --digest-sz 16 --auth-algo sha2-256-hmac --cipher-algo aes-cbc --cipher-op encrypt</code>
Command line (AES-GCM-128)	<code>./x86_64-native-linuxapp-gcc/app/dpdk-test-crypto-perf --socket-mem 2048,0 --legacy-mem --vdev crypto_aesni_gcm_pmd_1 -l 6,7 -n 2 -- --aad-key-sz 16 --buffer-sz 64,128,256,512,1024,2048 --optype aead --ptest throughput --aad-aad-sz 16 --devtype crypto_aesni_gcm --aad-op encrypt --burst-sz 32 --total-ops 10000000 --silent --digest-sz 16 --aad-algo aes-gcm --aad-iv-sz 12</code>
Notes	The SW PMD performance is linear scaling out with core numbers. The scale factor is around 1.

### Test Result:

Buffer Size (Bytes)	AES-CBC-128/SHA1-HMAC (Gbps)	AES-CBC-128/SHA2-256-HMAC (Gbps)	AES-GCM-128 (Gbps)
64	1.38	0.87	2.16
128	2.17	1.34	3.39
256	3.05	1.85	4.73
512	3.83	2.28	5.87
1024	4.38	2.58	6.66
2048	4.73	2.77	7.16





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Intel® AES-NI requires a computer system with an AES-NI enabled processor, as well as non-Intel software to execute the instructions in the correct sequence. AES-NI is available on select Intel® processors. For availability, consult your reseller or system manufacturer. **For more information, see <http://software.intel.com/en-us/articles/intel-advanced-encryption-standard-instructions-aes-ni/>**

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