



### **Background**

- . What is PCI-e EndPoint?
  - Target mode in PCI-express.
  - Common devices NIC, Graphic Cards, Security Coprocessors.
  - The PCI-e channel can be used for control or data plane.

#### • What is DPDK?

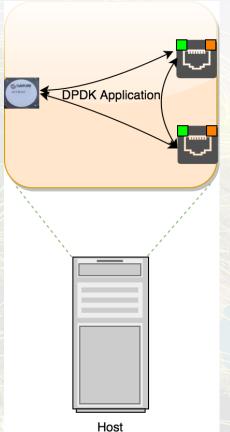
- · A set of libraries and drivers for fast packet processing.
- Enables third-party fast path stacks in Linux userspace.





# **Conventional DPDK Usage**

- Runs on standalone data plane processor
- DPDK application is Bus Master and owns the hardware

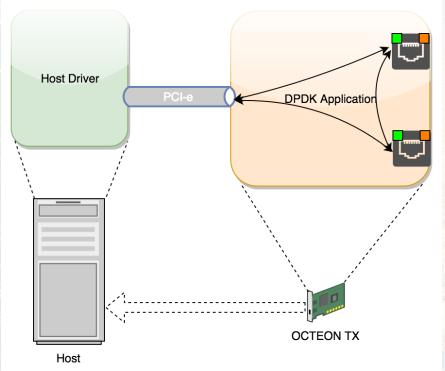






## **DPDK on an Intelligent NIC**

- DPDK runs on a coprocessor
- DPDK application is a PCI-e slave.







### Content on the PCI-e bus

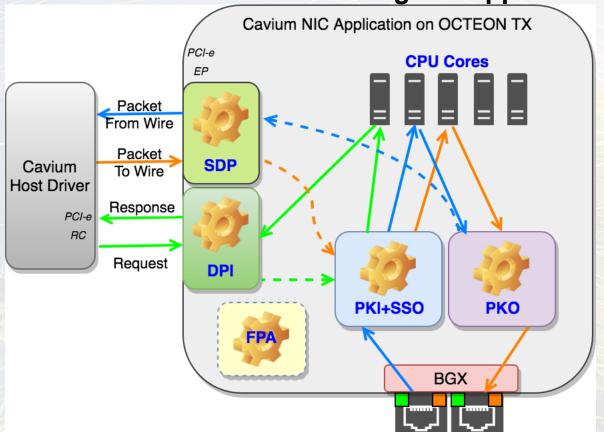
Networks Packet with L2,L3,etc headers

Custom message - typically as a request/response





**OCTEON TX as a PCI-e EP running NIC application** 







#### PCI-e EP as NIC

- Host projected as a NIC device
- Fits with the DPDKPMD model
- Register PCI-e EP as a RTE ETH DEV

```
//PMD driver for BGX
    static struct rte vdev driver bgx pmd drv = {
         .probe = bgx probe,
         .remove = bgx remove,
    };
    RTE PMD REGISTER VDEV(BGX PMD, bgx pmd drv);
    RTE PMD REGISTER PARAM STRING(BGX PMD, "nr port=<int> ");
    //PMD driver for SDP
    static struct rte vdev driver sdp pmd drv = {
11
         .probe = sdp probe,
12
         .remove = sdp remove,
13
    };
14
15
    RTE PMD REGISTER VDEV(SDP PMD, sdp pmd drv);
16
    RTE PMD REGISTER PARAM STRING(SDP PMD, "nr port=<int> ")
17
    //PMD driver for SSOW
18
    static struct rte_vdev_driver ssow_pmd drv = {
         .probe = ssow probe,
20
21
         .remove = ssow remove,
22
    };
23
24
    RTE PMD REGISTER VDEV(SSOW PMD, ssow pmd drv);
    RTE PMD REGISTER PARAM STRING(SSOW PMD, "nr port=<int> ")
```





#### PCI-e EP as NIC

- Use established DPDK API for:
  - Convenient
     registration of
     hardware blocks BGX, SDP, SSOW
  - Seamless send/recv of packets

```
// Application init-time action
    /** Port BGX0 **/
    ret = rte eth dev configure(bgx port, 0, nb tx queue, &port conf);
    ret = rte eth tx queue_setup(bgx_port, queueid, nb_txd, socketid, txconf);
    /** Port SDP0 **/
    ret = rte eth dev configure(sdp port, 0, nb tx queue, &port conf);
    ret = rte eth tx queue setup(sdp port, queueid, nb txd, socketid, txconf);
10
    /** Port SSOW0 **/
    ret = rte eth dev configure(ssow port, nb rx queue, 0, &port conf);
    ret = rte eth rx queue setup(ssow port, queueid, nb rxd, socketid, rxconf,
13
                                 pktmbuf pool[socketid]);
14
15
    // Application Control plane action
16
    ret = rte eth dev start(bgx port);
17
    ret = rte eth dev start(sdp port);
18
    ret = rte eth dev start(ssow port);
19
20
    // Application Data plane action
    /** Packet received from either Wire(BGX) or Host(SDP) **/
22
    nb rx = rte eth rx burst(ssow port, queueid, pkts burst, MAX PKT BURST);
23
24
    /** Use pkts burst[0].port to determine packet source **/
25
    /** Packet sent to Host over SDP **/
2.6
    nb tx = rte eth tx burst(sdp port, queueid, pkts burst, MAX PKT BURST);
    /** Packet sent to Wire over BGX **/
    nb tx = rte eth tx burst(bgx port, queueid, pkts burst, MAX PKT BURST);
```





### PCI-e EP as a Coprocessor

- For Custom Messages, host is projected as a coprocessor to DPDK.
- Coprocessor can be registered as a ethdev PMD, but it is not intuitive and not a clean fit.
- Coprocessor can better leverage the eventdev PMD.
- DPDK eventdev PMD is a Work In Progress.





# **Status**

#### Packet and Message exchanges

 Work in progress to adopt NIC firmware application to use DPDK's PMD model to use PCI-e as a Network Packet Interface and as a Coprocessor.

#### Roadmap

- Run performance benchmarks with NIC firmware running over DPDK and compare against non-DPDK NIC firmware implementation.
- Use eventdev PMD instead of ethdev for custom messages and even for Network packet exchange.





Q&A

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