









2017 Opening Agenda/Roadmap

Heqing ZHU, Intel







LEGAL DISCLAIMER

• No license (express or implied, by estoppel or otherwise) to any intellectual property rights is granted by this document.

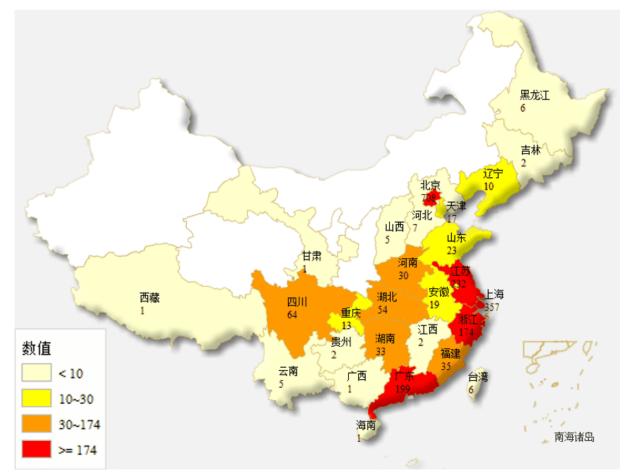
DPDK

3

- Intel disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade.
- This document contains information on products, services and/or processes in development. All information provided here is subject to change without notice. Contact your Intel representative to obtain the latest forecast, schedule, specifications and roadmaps.
- Intel technologies' features and benefits depend on system configuration and may require enabled hardware, software or service activation. Performance varies depending on system configuration. No computer system can be absolutely secure. Check with your system manufacturer or retailer or learn more at intel.com.
- © 2017 Intel Corporation. Intel, the Intel logo, Intel. Experience What's Inside, and the Intel. Experience What's Inside logo are trademarks of Intel. Corporation in the U.S. and/or other countries.
- *Other names and brands may be claimed as the property of others.
- Copyright © 2017, Intel Corporation. All rights reserved.







Welcome! This is 3rd year of DPDK summit in China.

Aim to provide a regular update on data plane features and future work, use cases. A growing data plane ecosystem worldwide,

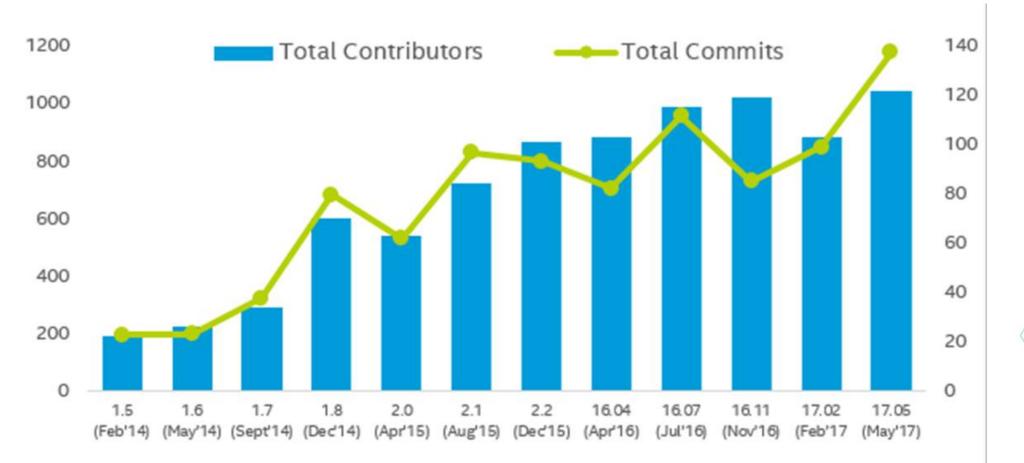
Please present on time.

Please ask questions and discuss offline



35 Tech blogs, 367 in group, 2400+ Subscribers, 32000+ read

The Growing DPDK Community



DPDK

5

SCOPNEV

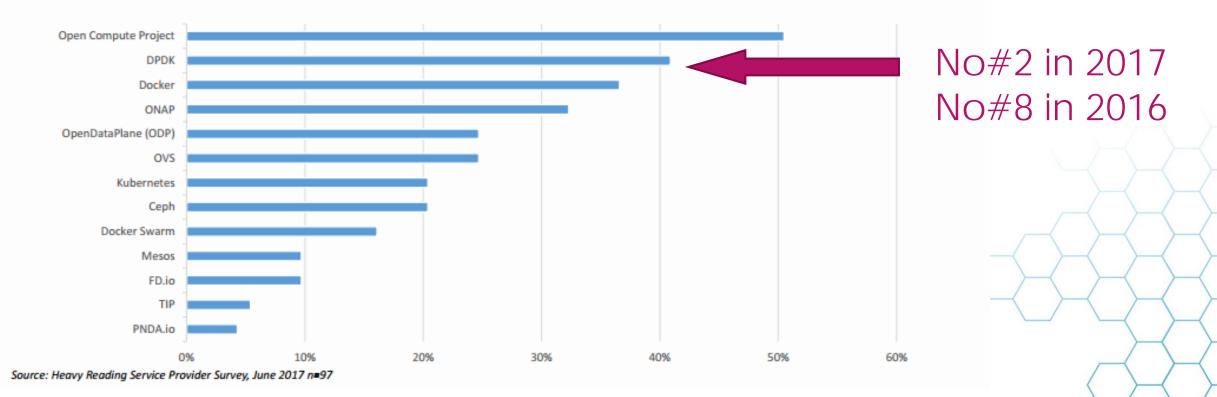
SUMMIT

JUNE 2017 BEIJING, CHINA

DPDK

6

"In addition to OpenStack and SDN controllers (e.g., OpenDaylight, ONOS, OpenContrail), which upstream projects are most important to the success of OPNFV?"





Move to Linux Foundation

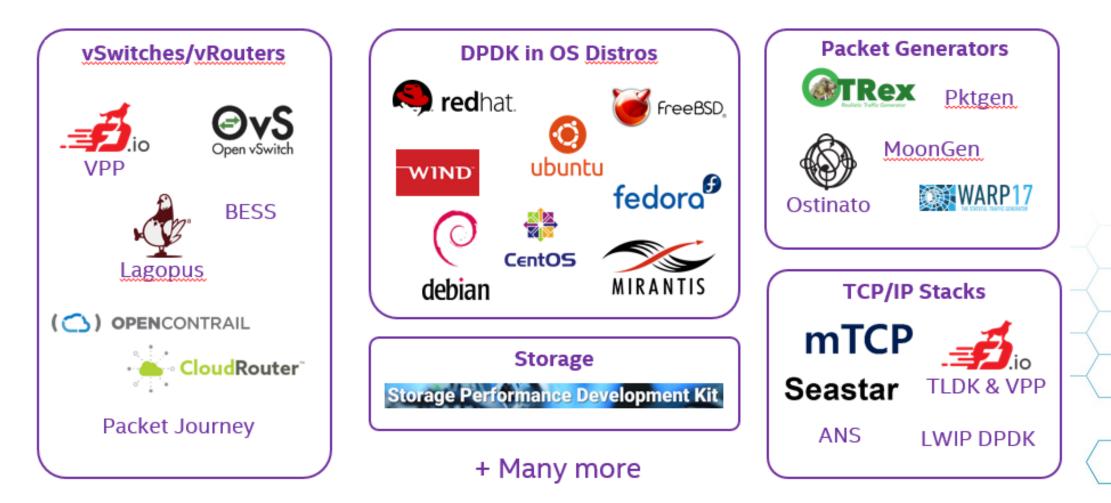


DPDK

8



DPDK Consumption





DPDK Governance by two boards

Governance for DPDK is provided by two boards

A Governing Board which deals with budget, marketing, lab resources, administrative, legal and licensing issues.

This includes representatives from the Gold project members, and a Silver member representative from Huawei. A Technical Board which deals with technical issues including approval of new sub-projects, deprecating old sub-projects, and resolution of technical disputes.

This includes representatives from Intel, Mellanox, 6WIND, Cavium, NXP, Microsoft and Brocade

These two boards are peers and work together to oversee the DPDK project.

- Project is still hosted at dpdk.org.
- Patch submission, review, and release processes in good shape.

DPDK 10



DPDK Performance Report

Attp://fast.dpdk.org/doc/perf/

P ▼ C @ Index of /doc/perf/

Index of /doc/perf/

/	2. Official p	performance reports are uploaded to DPDK.org
DPDK 16 11 Intel NIC performance report.pdf		
DPDK 17 02 Intel NIC performance report.pdf	04-Apr-2017 08:43	3 1M
DPDK 17 02 Intel virtio performance report.pdf	09-Mar-2017 11:22	2 885K
DPDK 17 02 Mellanox NIC performance report.pdf	12-May-2017 23:21	988K
DPDK 17 05 Intel NIC performance report.pdf	09-Jun-2017 14:49	9 1M
DPDK 17 05 Intel virtio performance report.pdf	09-Jun-2017 14:49	9 890K
Intel DPDK R16 11 NIC performance report.pdf	15-Dec-2016 12:51	1 764K

Home Download

Development Documentation

Mailing Lists Events

×

Performance Reports

- DPDK 17.05 Intel NIC Performance Report
- DPDK 17.05 Intel Vhost/Virtio Performance Report
- DPDK 17.02 Intel NIC Performance Report
- DPDK 17.02 Intel Vhost/Virtio Performance Report
- DPDK 17.02 Mellanox NIC Performance Report
- DPDK 16.11 Intel NIC Performance Report

17.05 reports have already been uploaded to the Performance Reports section of http://dpdk.org/doc.

News

1. Performance testing is performed at the end of each release

About

Open Lab

OD University of New Hampshire InterOperability Laboratory

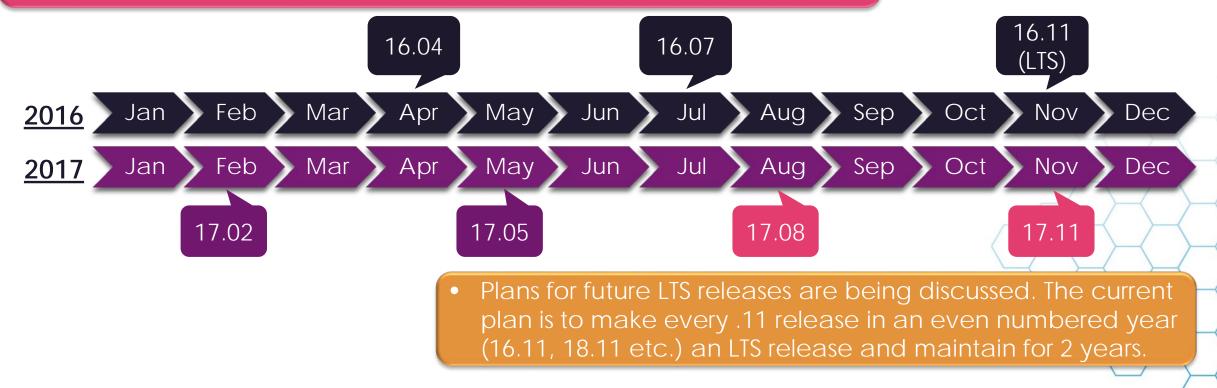
DPDK

- The Governing Board has approved budget for creation of a new DPDK lab.
- ▶ The aim is to do automated performance testing of patches before they're applied.
 - ▶ This will help to prevent cases where patches have unexpected performance impacts.
 - At the moment, any such performance impacts may not be identified until the end of a release cycle when performance testing is performed. This leaves very little time to fix the problems before the release.
 - Having performance data for patches before they're applied will help the maintainers to identify any problems and decide which patches should/should not be accepted.
- The plan is to host at the University of New Hampshire Interoperability Lab.



Release Plan

- Since 16.04, releases use the Ubuntu numbering scheme of YY.MM.
- We've transitioned from 3 major releases per year to 4 in 2017.
- Frequency and dates of releases will be fixed from 2017 onwards.





Future Roadmap, DPDK Framework

Cryptodev

- 1. Asymmetric support
- 2. Inline crypto

Compression

Discussion on the <u>dev@dpdk.org</u> mailing list and in the Tech Board on the mechanism for supporting programmable devices in DPDK.

Acceleration Model

- 1. Virtualization
- 2. Container

Data Pata APIs

- 1. Flow APIs
- 2. QoS APIs
- 3. Tunnel APIs
- 4. ---





Agenda for Morning

Time	Durations	Presenter	Company	Торіс
8:30 - 8:45	15	Heqing Zhu	Intel	Opening
8:45 - 9:10	25	Jianfeng Tan	Intel	DPDK in container: Status Quo and Future Directions
9:10 - 9:30	20	Hailong Wang	Tencent	F-Stack, a full user space network service on DPDK
9:30 - 10:00	30	Cunming Liang	Intel	A Better Virtio towards NFV Cloud
10:00 - 10:20	20	Changpeng Liu/Xing Zen	Intel	Accelerate VM I/O via SPDK and Crypto for Generaic vHost
10:20 - 10:40	20	Break		
10:40 - 11:15	35	Huai Huang	Meituan	OVS-DPDK Practices in Meituan Cloud
11:15 - 11:45	30	Fangliang Lou	ZTE	Network performance tuning, lesson learned.
11:45 - 12:15	30	Liang Ma	Intel	OPDL: On The Path To Packet Processing Nirvana



Agenda for Afternoon

Time	Durations	Presenter	Company	Торіс	
13:30 - 14:10	40	Helin Zhang Jingjing Wu	Intel	Intel® 25GbE Ethernet Adapter Advanced Features for NFV, Adaptive VF	
14:10 - 14:30	20	Fan Zhang	Intel	Accelerate VPP workload with DPDK Cryptodev Framework	
14:30 - 14:45	15	Haohao Zhang	Tencent	Data Center Security Use Case with DPDK	
14:45 - 15:15	30	Yunhong Jiang Wei Wang	Intel	Towards Low Latency Interrupt Mode PMD	
15:15 - 15:35	20	Break			
15:35 - 16:05	30	Hao Lin	T1Networks	Telco data plane status, challenges and solutions	
16:05 - 16:35	30	Zhaohun Sun	Panabit	Support Millions users in vBRAS	
16:35 - 16:50	15	Jie Zheng	United Stack	A High speed DPDK PMD approach in LXC	
16:50 - 17:20	30	Kai Wang	Yunshan	Cloud Data Center, Network Security practices	
17:20 - 17:50	30	DPDK Box Lucky Draw, Social			





Further Info

- Open source website (<u>dpdk.org</u>):
 - Download the code, access the documentation, join the mailing lists etc.
- DPDK Summit events:
 - Includes videos and presentations from previous events.
 - Subscribe to quarterly newsletter.
- Videos and training:
 - Intel® Network Builders University
 - BrightTalk webinars
- Meet-ups
- Interested in contributing?
 - Subscribe to the <u>mailing lists</u>.
 - Review the <u>Contributor's Guidelines</u> and contribute patches!





Thanks!!



欢迎关注DPDK开源社区