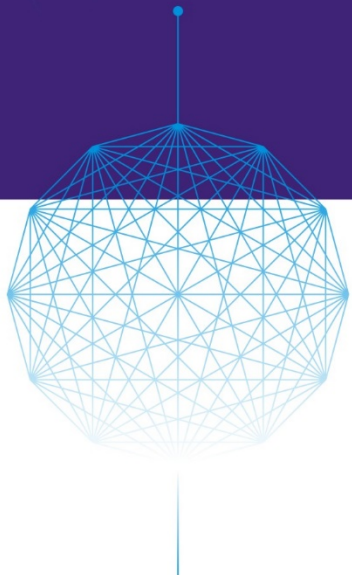







# DPDK SUMMIT CHINA 2017



主办方：

参与方： 腾讯云  ZTE  美团云  Panabit®  太一星辰  UnitedStack 联合云  云杉网络 Yunshan Networks


协办方： SDNLAB 专注网络创新技术 视频支持方： IT大咖说 网络全媒平台






# Accelerating the FD.IO/VPP Crypto Workload with the DPDK Cryptodev Framework

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主办方: 

参与方:  腾讯云  ZTE  美团云  Panabit  太一星展  云杉网络

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## Agenda

- ▶ Problem Statement
- ▶ DPDK Cryptodev Framework Introduction
- ▶ Enable DPDK Cryptodev Framework in VPP
- ▶ Performance
- ▶ Future work
- ▶ Conclusion





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*Think about security at every step of the process: architecture, implementation, testing, documentation, distribution and deployment*

- *Dr. Nicko van Someren, CTO, Linux Foundation*

With VPP, a single core can do 40G, 100G or even higher throughput L2 forwarding

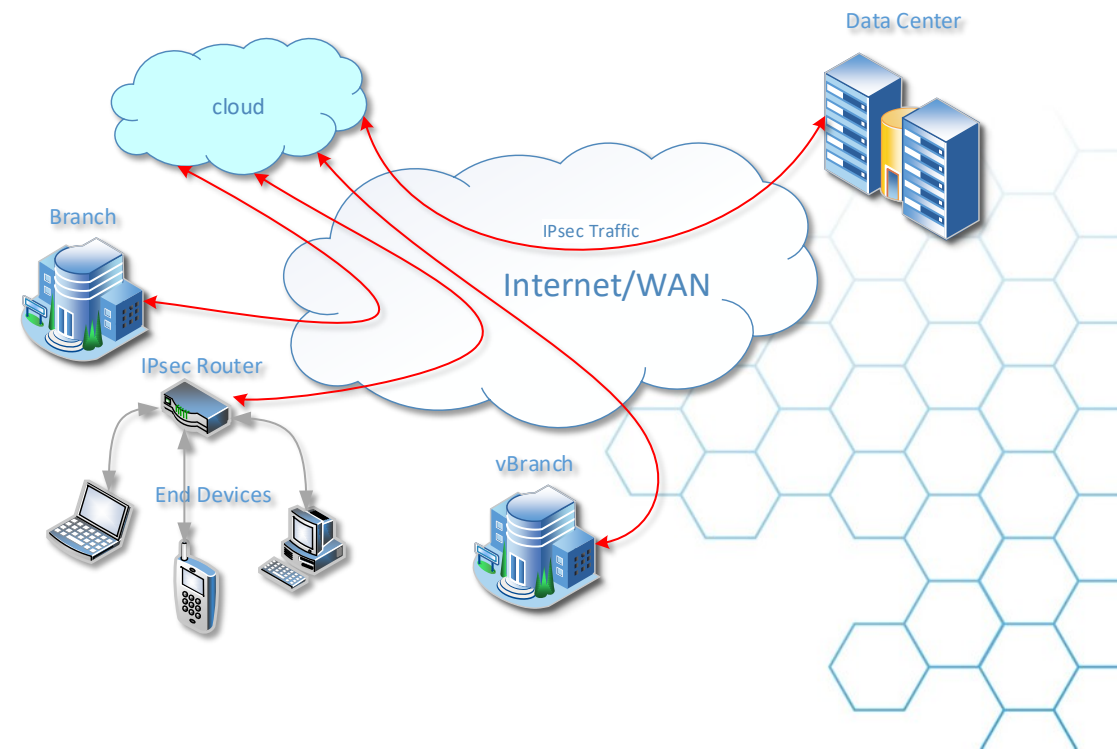
But what is the throughput after adding security protection?





# Let's take IPsec as an example

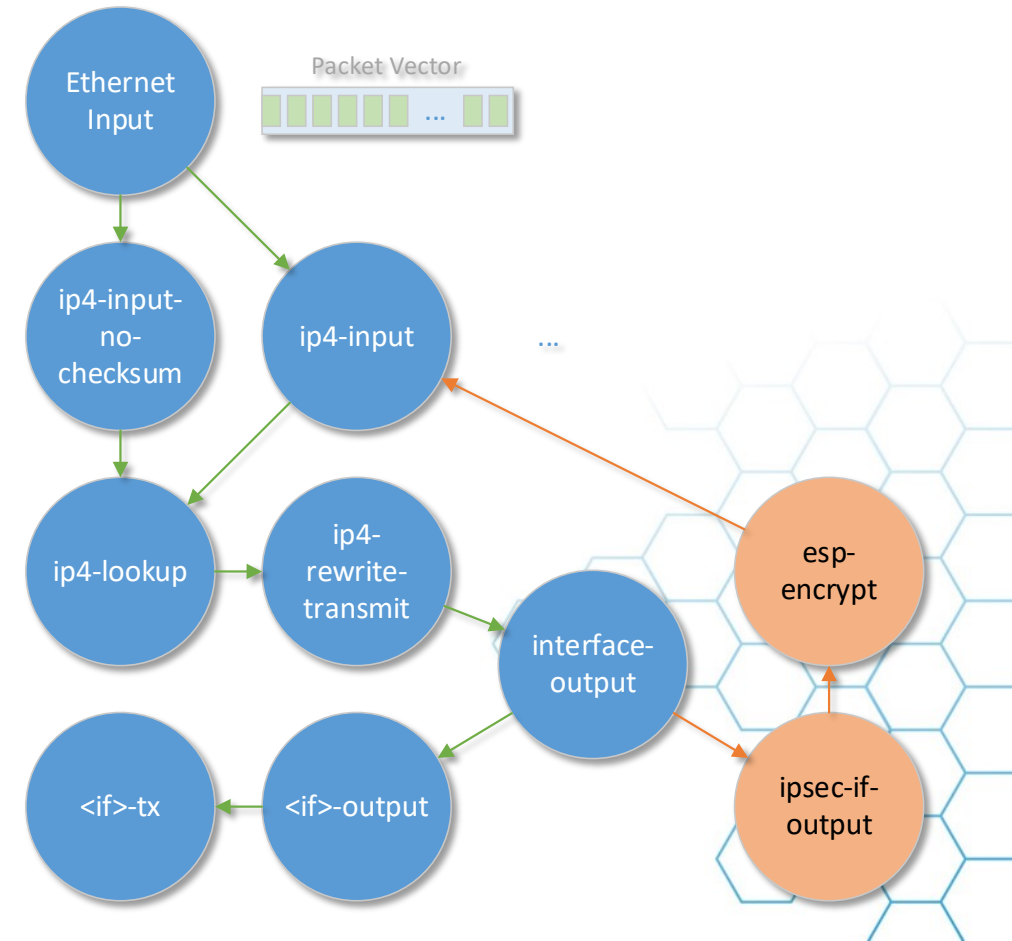
- ▶ > 20 years old but is still extremely popular
- ▶ Playing the role of security guardian in many network applications
- ▶ Requires lots of computations including crypto
- ▶ When traffic rate is high, efficient crypto implementation becomes necessary





## FD.io / VPP IPsec

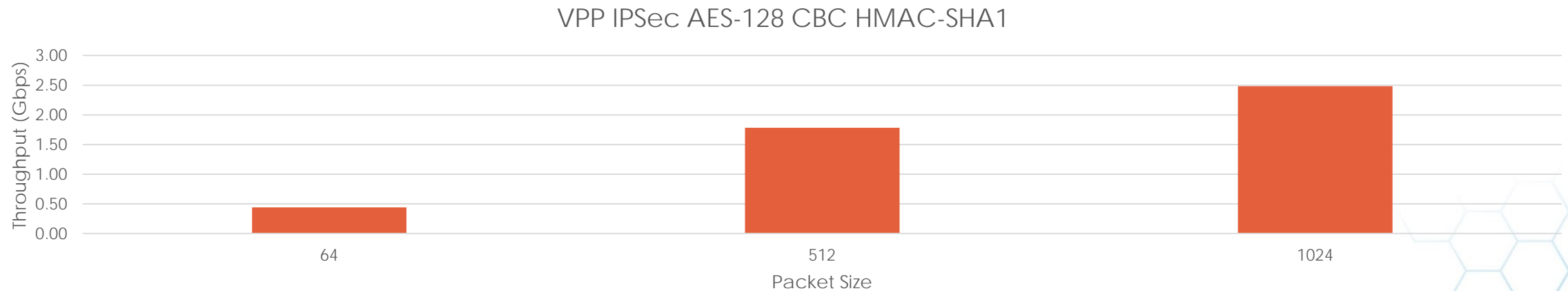
- ▶ Supports IPv4/IPv6 IPsec ESP, tunnel/transport mode, and SA management
- ▶ DPDK EthDev integrated
- ▶ For crypto it uses OpenSSL by default
- ▶ Performance?







# FD.io/VPP IPsec with OpenSSL as Crypto Performance<sup>s</sup>



- ▶ Does Securing the Network Application have to degrade performance?
- ▶ **Not Really**





## Agenda

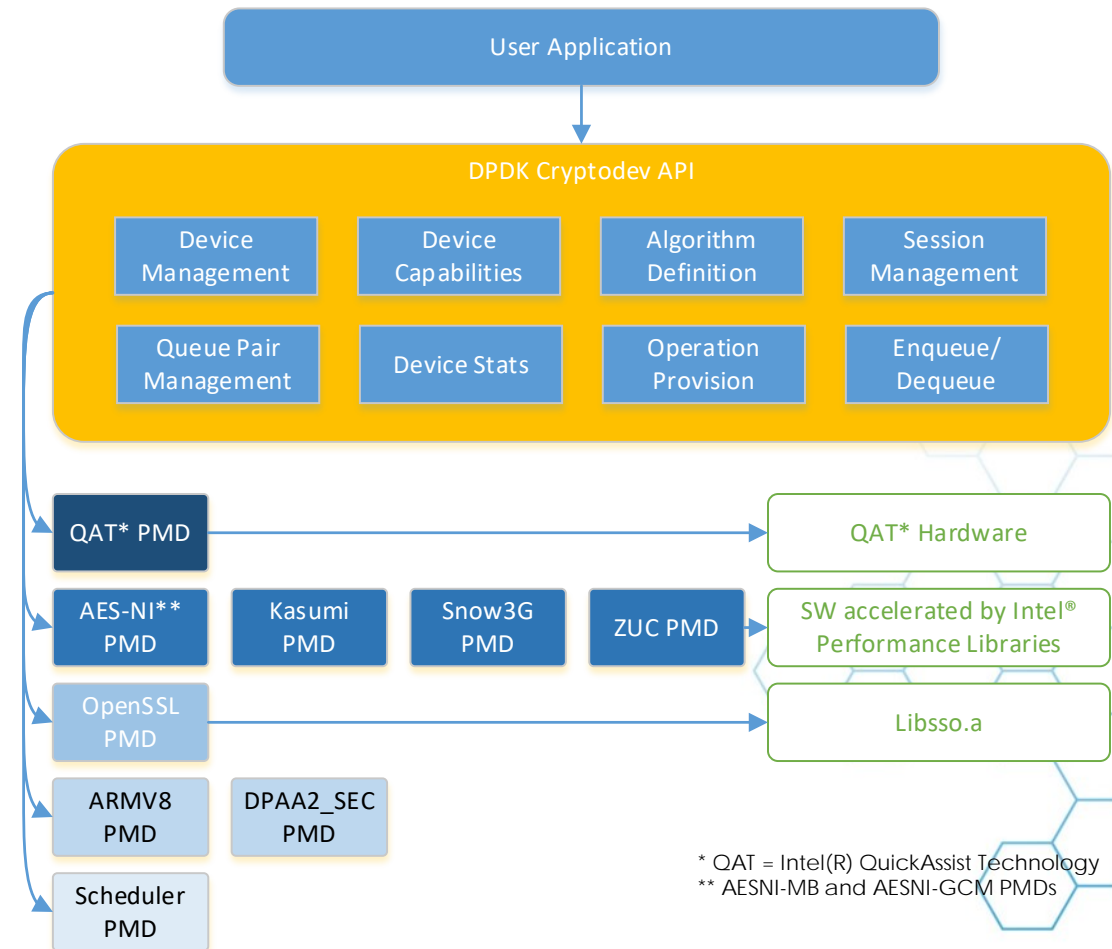
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## DPDK Cryptodev Framework

- ▶ Crypto framework for processing symmetric crypto workloads in DPDK.
- ▶ DPDK Cryptodev consists of:
  - ▶ SW and HW Crypto PMDs
  - ▶ A standard API supports all PMDs
  - ▶ Multi-queues for multi-thread sharing
- ▶ Effortless migration (SW-HW, PHY-VIRT)
- ▶ Asynchronous enqueue/dequeue



\* QAT = Intel(R) QuickAssist Technology  
 \*\* AESNI-MB and AESNI-GCM PMDs



# Supported Algorithms In Cryptodev

## Cipher Algorithms

- **AES CBC/CTR**  
128/192/256 bit
- **Snow3G (UEA2)**
- **KASUMI F8,**
- **ZUC EEA3**
- **AES\_CFB**
- **NULL**

## Hash Algorithms

- **MD5\_HMAC\***
- **SHA1/224\*/256/384\*/512,**
- **AES XCBC,**
- **Snow3G UIA2,**
- **KASUMI F9,**
- **ZUC EIA3,**
- **NULL**

## AEAD Algorithms

- **AES GCM 128/192\*\*/256 bit**

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\*\* AESNI-MB and AESNI-GCMPMDs



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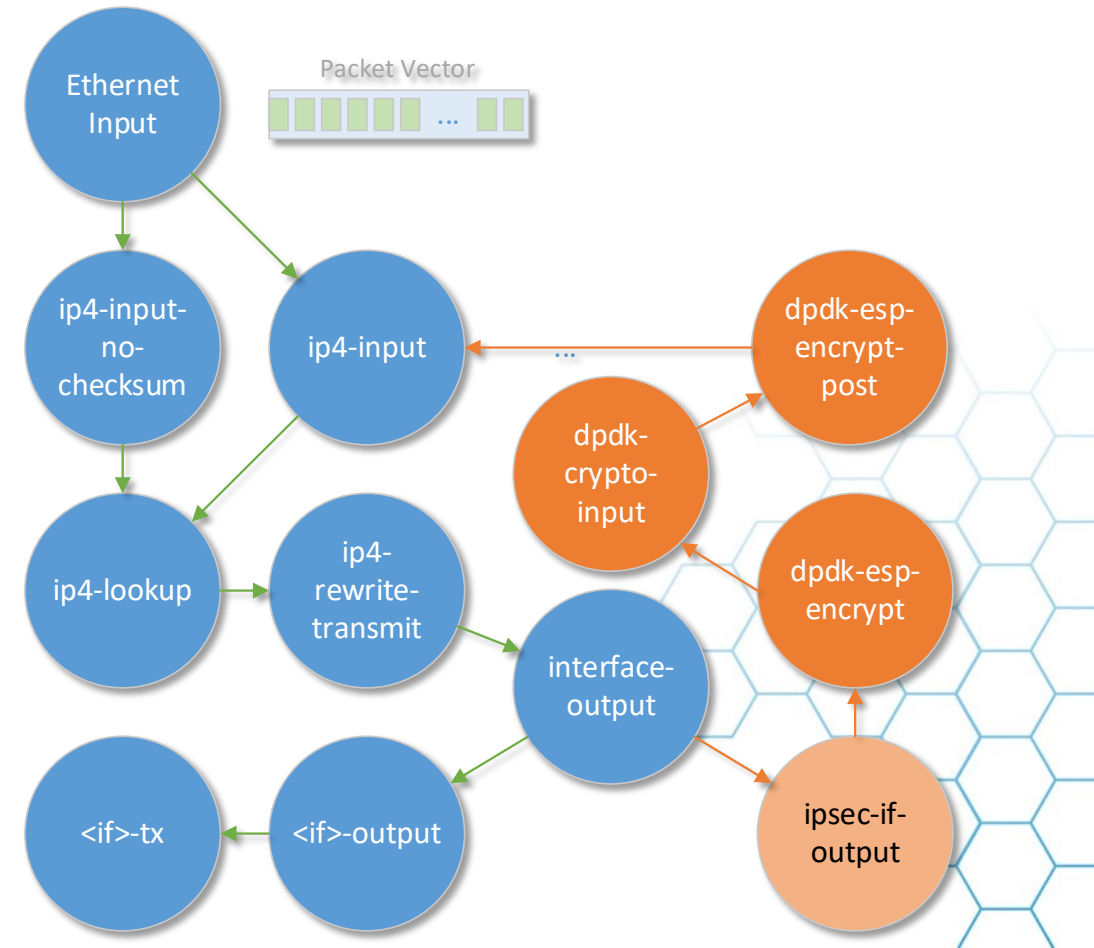






# Enable DPDK Cryptodev in VPP IPsec

- ▶ Replaced 2 nodes:
  - ▶ esp-encrypt → dpdk-esp-encrypt
  - ▶ esp-decrypt → dpdk-esp-decrypt
- ▶ Added 3 nodes:
  - ▶ dpdk-crypto-input
  - ▶ dpdk-esp-encrypt-post
  - ▶ dpdk-esp-decrypt-post





# VPP Configuration for DPDK Cryptodev

- ▶ Environmental option:
  - ▶ For software PMD:  
`vpp_use_dpdk_cryptodev_sw=yes`
- ▶ User only needs to provide Cryptodevs in startup.conf file
- ▶ Allocate crypto resources on best effort approach
- ▶ No special IPsec configuration is required
- ▶ More information can be found [here](#)

## Sample Configuration:

```
dpdk {  
    ...  
    #HW PMDs  
    enable-cryptodev  
    dev 0000:85:01.0  
    dev 0000:85:01.1  
    #SW PMDs  
    vdev cryptodev_aesni_mb_pmd0,socket_id=1  
    vdev cryptodev_aesni_mb_pmd1,socket_id=1  
}
```



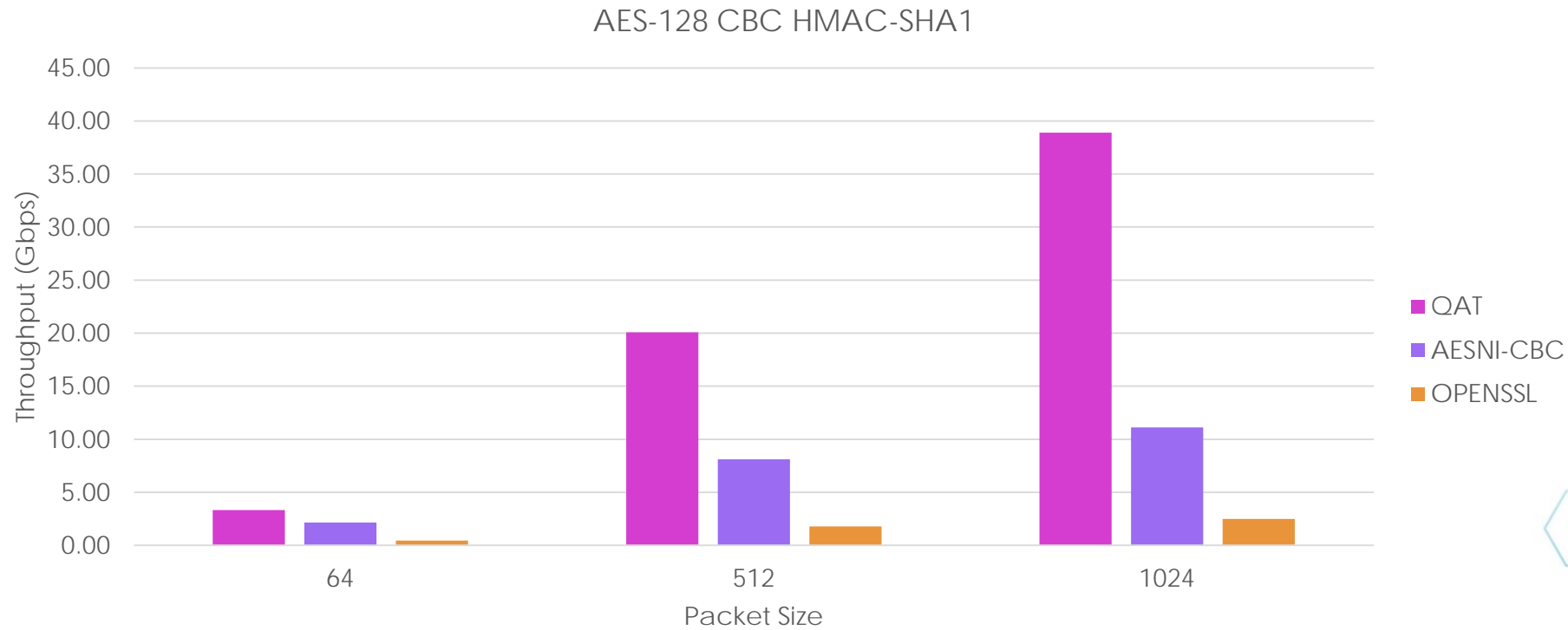
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## Performance<sup>§</sup> from VPP IPsec



\*QAT = Intel(R) QuickAssist Technology

<sup>§</sup> Tests document performance of components on a particular test, in specific systems. Differences in hardware, software, or configuration will affect actual performance. Consult other sources of information to evaluate performance as you consider your purchase. For more complete information about performance and benchmark results, visit [www.intel.com/benchmarks](http://www.intel.com/benchmarks)

Test and System Configurations: Estimates are based on internal Intel analysis using at least Data Plane Development Kit IPsec sample application on Intel(R) Xeon(R) CPU E5-2695 v4@ 2.10GHz with atleast using Intel(R) Communications Chipset(s) 8955 with Intel(R) QuickAssist Technology.



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## Future Work

- ▶ DPDK Cryptodev Optimization
- ▶ Enable DPDK Cryptodev Framework in VPP IKEv2.
- ▶ VPP IPsec Performance Tuning
- ▶ Enable DPDK Cryptodev Scheduler PMD to increase crypto workload processing capability per-worker thread
- ▶ Virtio-Crypto Enabling





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## Summary

- ▶ Achieved VPP IPsec Performance boost by enabling DPDK Cryptodev Framework
- ▶ QAT hardware accelerated VPP IPsec has more performance boost than the software alternative
- ▶ Seamlessly integrated into VPP, easy to enable and configure, no extra IPsec configuration is required
- ▶ Migration between Software and Hardware, Physical and Virtual, is effortless





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## Q&A

# Thanks!!



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